

defining a degree of excellence

Surface Mount PTC RoHS6 Compliant & Halogen-Free

1812 Chip

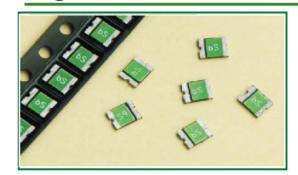
0ZCG Series

HF (Pb)

0ZCG May2014D

0ZCGSeries Fuse





Application

All high-density boards

Product Features

1812 Chip Size, Fast Trip Time, Low DCR Resistance

Operating (Hold Current) Range

100mA - 3A

Maximum Voltage

6-60V (per table)

Temperature Range

-40°C to 85°C

Agency Approval

TUV (Std. EN60738-1-1, Cert. R50102117& R50102187) UL Component (Std. UL1434, File E305051) **UL Conditions of Acceptability:**

1. These devices have been investigated for use in safety circuits and are suitable as a limiting device. 2. These devices have been calibrated to limit the current to 8 amps within 5 seconds, per ANSI/NFPA 70, "National Electrical Code"

HALOGEN FREE = |HF





		Hold	Trip	Rated	Maximum	Typical	Max Time to Trip		Resistance Tolerance		Agency Approvals	
	Part Number	Current Curren	Current	Voltage Vmax, Vdc	Current Imax, A	Power Pd, W	Current Time	Time	ime Rmin	R1max Ohms	c FLL °us	Α̈́τΰν
			IT, A					Sec				
Α	0ZCG0010FF2C	0.10	0.30	60	100	0.8	8.0	0.020	1.600	15.00	Υ	
В	0ZCG0014FF2C	0.14	0.30	60	100	0.8	8.0	0.008	1.200	6.500	Υ	Υ
С	0ZCG0020FF2C	0.20	0.40	30	100	0.8	8.0	0.020	0.800	5.000	Υ	Υ
C	0ZCG0020AF2C	0.20	0.40	60	100	0.8	8.0	0.020	0.800	5.000	Υ	
D	0ZCG0030FF2C	0.30	0.60	30	100	0.8	8.0	0.100	0.200	1.750	Υ	
Е	0ZCG0035FF2C	0.35	0.70	16	100	0.8	8.0	0.100	0.320	1.500	Y	Υ
_	0ZCG0035AF2C	0.35	0.70	30	100	0.8	8.0	0.100	0.320	1.500	Υ	
F	0ZCG0050FF2C	0.50	1.00	16	100	0.8	8.0	0.150	0.150	1.000	Υ	Υ
F	0ZCG0050AF2C	0.50	1.00	30	100	0.8	8.0	0.150	0.150	1.000	Υ	
	0ZCG0075FF2C	0.75	1.50	16	100	0.8	8.0	0.200	0.110	0.450	Υ	Υ
G	0ZCG0075AF2B	0.75	1.50	24	100	1.0	8.0	0.200	0.110	0.290	Υ	Υ
	0ZCG0075BF2B	0.75	1.50	33	100	1.0	8.0	0.200	0.110	0.400	Υ	Υ
	0ZCG0110FF2C	1.10	2.20	8	100	0.8	8.0	0.300	0.040	0.210	Υ	Υ
Н	0ZCG0110AF2C	1.10	2.20	16	100	0.8	8.0	0.500	0.040	0.180	Υ	Υ
	0ZCG0110BF2B	1.10	2.20	24	100	1.0	8.0	0.500	0.060	0.200	Υ	Υ
1	0ZCG0125FF2C	1.25	2.50	6	100	0.8	8.0	0.400	0.050	0.140	Υ	Υ
ī	0ZCG0125AF2B	1.25	2.50	16	100	0.8	8.0	0.400	0.050	0.140	Υ	
	0ZCG0150FF2C	1.50	3.00	8	100	0.8	8.0	0.500	0.040	0.110	Υ	Υ
J	0ZCG0150AF2C	1.50	3.00	12	100	1.0	8.0	0.500	0.040	0.110	Υ	Υ
	0ZCG0150BF2C	1.50	3.00	24	100	1.0	8.0	1.500	0.040	0.120	Υ	Υ
	0ZCG0160FF2C	1.60	3.20	8	100	0.8	8.0	0.500	0.030	0.100	Υ	Υ
K	0ZCG0160AF2C	1.60	3.20	12	100	1.0	8.0	1.000	0.030	0.100	Υ	Υ
	0ZCG0160BF2C	1.60	3.20	16	100	1.0	8.0	1.000	0.030	0.100	Υ	Υ
L	0ZCG0200FF2C	2.00	3.50	8	100	1.0	8.0	2.000	0.020	0.070	Υ	Υ
	0ZCG0260FF2C	2.60	5.00	8	100	1.0	8.0	2.500	0.015	0.047	Υ	Υ
M	0ZCG0260AF2B	2.60	5.00	13.2	100	1.3	8.0	5.000	0.015	0.050	Y	Υ
	0ZCG0260BF2B	2.60	5.00	16	100	1.3	8.0	5.000	0.015	0.050	Υ	Υ
N	0ZCG0300FF2B	3.00	5.00	6	100	1.0	8.0	4.000	0.012	0.040	Y	Υ

Hold Current-maximum current at which the device will not trip in still air at 23°C. lн

Trip current-minimum current at which the device will always trip in still air at 23°C. Maximum fault current device can withstand without damage at rated voltage (Vmax). **Imax**

Maximum voltage device can withstand without damage at its rated current. Vmax

Pd Typical power dissipated by device when in tripped state in 23°C still air environment.

Rmin Minimum device resistance at 23°C.

R1max Maximum device resistance at 23°C, 1 hour after initial device trip, or after being soldered to

PCB in end application.

Specifications subject to change without notice

Surface Mount PTC *0ZCG Series*

1812 Chip
RoHS6 Compliant & Halogen-Free



OZCG May2014C

PTC's – Basic Theory of Operation / "Tripped" Resistance Explanation

Fundamentally, a Bel PTC consists of a block of polymeric material containing conductive filler and bonded between two conductive, planar terminations.

At currents below the device IHOLD rating, AND at temperatures below 100C, the PTC maintains a resistance value below its R1 MAX rating.

As the device's temperature approaches 130C, either due to an increase in ambient temperature or a current exceeding its I TRIP rating, volumetric expansion of the filled polymer breaks apart the majority of conductive pathways across the terminals created by chain contact of adjacent filler particles and device resistance increases sharply by several orders of magnitude.

At the much higher "Tripped" resistance, there is just enough leakage current to allow internal heating to "hold" the device in its tripped state (around 125C) until power is interrupted. Once power is removed, the PTC's core cools and contracts allowing conductive chains to reform and return the device to its low resistance state.

The catalog data for each device specifies a **'Typical Power'** value. This is the power required to exactly match the heat lost by the tripped device to its ambient surroundings at 23C. By Ohm's Law, power can be stated as: W = E^2/R. Thus the approximate resistance of a "Tripped" PTC can be determined by:

 $R = E^2/W$, where "E" is the voltage appearing across the PTC (usually the supply's open circuit voltage), and "W" is the **Typical Power** value for the particular PTC.

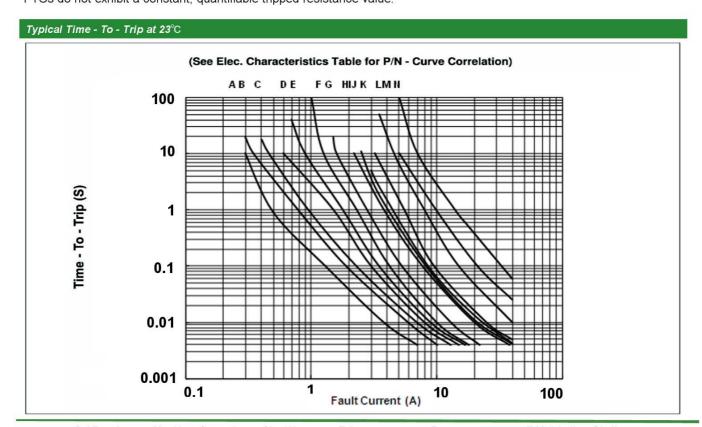
Since the PPTC acts to maintain a constant internal temperature, its apparent resistance will change based upon applied voltage and, to a lesser degree, ambient conditions. Consider the following example....

A PTC with a **Typical Power** of 1 watt protecting a circuit using a 60V supply will demonstrate an apparent, tripped resistance "R" of:

 $R = 60^2/1 = 3,600 \text{ ohms}$

This same tripped device when used to protect a 12V circuit would now present an apparent resistance of: $R = 12^2/1 = 144$ ohms

The value for Typical Power is "typical" because any physical factors that affect heat loss (such as ambient temperature or air convection) will somewhat alter the level of power that the PTC needs to maintain its internal temperature. In short, PTCs do not exhibit a constant, quantifiable tripped resistance value.



Bel Fuse Inc. 206 Van Vorst Street, Jersey City, NJ 07302 • Tel: 201-432-0463 • Fax: 201-432-9542 • E-Mail: belfuse@belf.com

Website: www.belfuse.com



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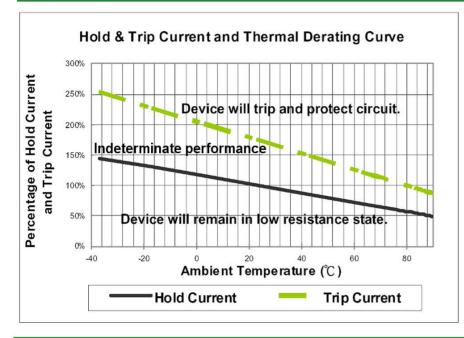
Surface Mount PTC

0ZCG Series

RoHS6 Compliant & Halogen-Free



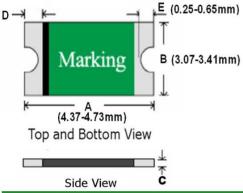
Thermal Derating Curve



Cautionary Notes

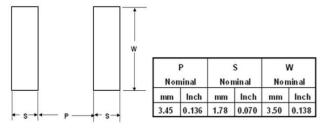
- Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.
- These Polymer PTC (PPTC) devices are intended for protection against occasional overcurrent/ overtemperature fault conditions and may not be suitable for use in applications where repeated and/ or prolonged fault conditions are anticipated.
- Avoid contact of PTC device with chemical solvent. Prolonged contact may adversely impact the PTC performance.
- These PTC devices may not be suitable for use in circuits with a large inductance, as the PTC trip can generate circuit voltage spikes above the PTC rated voltage.
- These devices are intended for use in DC voltage applications only. Use in AC voltage applications should be first discussed with Bel Fuse engineering
- Not recommended for use on potted or conformal coated PCB's. Restriction of free air flow could affect electrical performance and/or result in device failure Consult Bel Fuse engineering.
- In the "Indeterminate Performance / grey zone", tripping may occur but cannot be relied upon. For special circumstances considering use within this region, consult Bel Fuse Engineering.
- 8. MSL: 2a (According to IPC J-Std-020).

Mechanical Dimensions and Marking



Pad Layout

The dimensions in the table below provide the recommended pad layout.



Termination Pad Materials

Matte Tin-plated Copper

All dimensions in mm.

		Dimer	Marking Code				
Part Number	С		9	D	"b" , Incode		
	Min	Max	Min	Max	b xxxx	b xxx	
0ZCG0010FF2C	0.60	0.90	0.30	0.95	0010		
0ZCG0014FF2C	0.60	0.90	0.30	0.95	0014		
0ZCG0020FF2C	0.60	0.90	0.30	0.95	0020		
0ZCG0020AF2C	0.60	0.90	0.30	0.95		020 60	
0ZCG0030FF2C	0.40	0.70	0.30	0.95	0030		
0ZCG0035FF2C	0.40	0.70	0.30	0.95	0035		
0ZCG0035AF2C	0.40	0.70	0.30	0.95		035 30	
0ZCG0050FF2C	0.35	0.65	0.30	0.95	0050		
0ZCG0050AF2C	0.45	0.75	0.30	0.95		050 30	
0ZCG0075FF2C	0.35	0.65	0.30	0.95	0075		
0ZCG0075AF2B	0.80	1.55	0.25	0.95		075 24	
0ZCG0075BF2B	0.80	1.55	0.25	0.95		075 33	
0ZCG0110FF2C	0.25	0.55	0.30	0.95	0110		
0ZCG0110AF2C	0.25	0.90	0.30	0.95		110 16	
0ZCG0110BF2B	0.80	1.30	0.25	0.95		110 24	
0ZCG0125FF2C	0.25	0.55	0.30	0.95	0125		
0ZCG0125AF2B	0.50	1.00	0.30	0.95		125 16	
0ZCG0150FF2C	0.25	0.55	0.30	0.95	0150	1	
0ZCG0150AF2C	0.60	1.10	0.25	0.95		150 12	
0ZCG0150BF2C	0.60	1.55	0.25	0.95		150 24	
0ZCG0160FF2C	0.25	0.90	0.30	0.95	0160		
0ZCG0160AF2C	0.60	1.35	0.25	0.95		160 12	
0ZCG0160BF2C	0.60	1.35	0.25	0.95		12 160 16	
0ZCG0200FF2C	0.55	1.20	0.25	0.95		200 A	
0ZCG0260FF2C	0.55	1.20	0.25	0.95	0260		
0ZCG0260AF2B	0.80	1.55	0.25	0.95		260 13	
0ZCG0260BF2B	0.80	1.55	0.25	0.95		260 16	
0ZCG0300FF2B	0.80	1.55	0.25	0.95	0300	1	

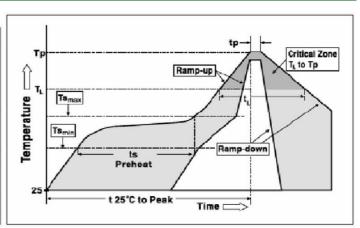
Specifications subject to change without notice



0ZCG May2014D

Solder Reflow and Rework Recommendations

Profile Feature	Pb-Free Assembly		
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/second max.		
Preheat :			
Temperature Min (Tsmin)	150 ℃		
Temperature Max (Tsmax)	200 ℃		
Time (tsmin to tsmax)	60-180 seconds		
Time maintained above:			
Temperature(T _L)	217 °C		
Time (t _L)	60-150 seconds		
Peak/Classification Temperature(Tp):	260 ℃		
Time within 5℃ of actual Peak :			
Temperature (tp)	20-40 seconds		
Ramp-Down Rate :	6 °C/second max.		
Time 25 ℃ to Peak Temperature :	8 minutes max.		



Solder Reflow

Due to " lead free / RoHS6 " construction of these PTC devices , the required Temperature and Dwell Time in the " Soldering "zone of the reflow profile are greater than those used for non-RoHS devices.

- 1.Recommended reflow methods; IR, vapor phase oven, hot air oven.
- 2. Not Recommended For Wave Solder / Direct Immersion.
- 3. Recommended maximum; paste thickness is 0.25mm.
- 4. Devices are compatible with standard industry cleaning solvents and methods.

Caution

If reflow temperature / dwell times exceed the recommended profile, the electrical performance of the PTC may be affected.

Rework: MIL-STD-202G Method 210F. Test Condition A.

Standard Packaging

Part Number	Tape/Reel Qty			
0ZCG0010FF2C				
Thru	2,000			
0ZCG0075FF2C				
0ZCG0075AF2B	1,500			
0ZCG0075BF2B	1,500			
0ZCG0110FF2C	2,000			
0ZCG0110AF2C				
0ZCG0110BF2B	1,500			
0ZCG0125FF2C				
Thru	2,000			
0ZCG0260FF2C				
0ZCG0260AF2B	1,500			
Thru				
0ZCG0300FF2B				

2000 or 1500 fuses in 7 inches dia. Reel, 8mm wide tape, 4mm pitch, per EIA-481 (equivalent IEC-286 part 3).

	0ZCG	0XXX	XX	XX
PTC series				
0ZCG, 1812 Size				
I HOLD Rating				
Refer to Part Number and IH Rating in I	Electrical Characteris	tics Table on P.1	la:	
Eletrical Characteristics				
F = Standard Design				
A to Z (except F) = Special, customer sp	oec, DCR sort, etc.			
Mechanical Features				
F = Standard Design				
A to Z (except F) = Special, customer sp	oec, lead forming, etc			
Tape & Reel Qty				
See standard packaging				

Specifications subject to change without notice

CORPORATE OFFICE

Bel Fuse Inc.

206 Van Vorst Street Jersey City, NJ 07302 Tel 201-432-0463 Fax 201-432-9542 E-Mail: belfuse@belf.com Website: www. belfuse.com

FAR EAST OFFICE

Bel Fuse Ltd.

8/F Luk Hop Industrial Building 8 Luk Hop Street San Po Kong Kowloon, Hong Kong Tel 852-2328-5515 Fax 852-2352-3706

EUROPE

Bel Stewart GmbH

Industriestrasse 20 61381 Friedrichsdorf Germany Tel 49-6172-9552-0 Fax 49-6172-9552-40