

iW3617 Product Brief

AC/DC Digital Power Controller for High Power Factor Dimmable LED Drivers



1.0 Features

- Isolated/non-isolated offline 120V_{AC}/230V_{AC} LED driver up to 25W output power
- Wide line frequency ranges (from 45Hz to 66Hz)
- Meets IEC61000-3-2 requirement
- Total harmonic distortion < 15% with PF > 0.95
- Wide dimmer compatibility
 - » Leading-edge dimmer
 - » Trailing-edge dimmer
 - » Digital dimmer
 - » Occupancy sensors and timers
- Under 20% 100Hz/120Hz output ripple current
- Wide dimming range from 1% to 100%
- Flickerless™ LED dimming
- Resonant control to achieve high efficiency (typical > 85% without dimmer)
- Over-temperature LED current foldback
- Small solution size
 - » Two-stage topology enables small-size input and output filter capacitors
 - » 200kHz maximum switching frequency enables small transformer
 - » Intelligent dimmer interface eliminates dedicated high-power bleeder
- Primary-side sensing eliminates the need for opto-isolator feedback
- Tight LED current regulation ($\pm 5\%$)
- Fast start-up (< 0.5s without dimmer)
- Supports hot-plug LED module (Zhaga)
- Compatible with NEMA SSL6 dimming curve standard
- Supports wide LED output voltage range
- Multiple protection features:
 - » LED open-circuit and short-circuit protection
 - » Over-current and over-temperature protection
 - » Current sense resistor short-circuit protection
 - » AC line over voltage/frequency protection

2.0 Description

The iW3617 is a two-stage, high-performance AC/DC offline power supply controller for dimmable LED luminaires. It applies advanced digital control technology to detect the dimmer type and phase, which provides dynamic impedance to interface the dimmer and control the LED brightness at the same time. The iW3617 uses iWatt's unique digital Flickerless™ technology to eliminate visible flicker in the entire dimming range and minimize low frequency output ripple current.

With advanced dimmer detection technology, the iW3617 can operate with most wall dimmers without the need for a high-power bleeder, including leading-edge dimmers (R-type or R-L type) and trailing-edge dimmers (R-C type). In addition, the iW3617's cycle-by-cycle waveform analysis technology allows fast dimmer setting response. When no dimmer is on the line, the iW3617 optimizes the power factor and minimizes the current harmonic distortion to the AC line.

The iW3617 operates the main power converter that delivers constant current to the LED load in quasi-resonant mode to provide high power efficiency and minimize electro-magnetic interference (EMI). It uses iWatt's patented PrimAccurate™ primary-side sensing technology to achieve excellent LED current regulation under different line and LED load voltages, without using a secondary-side feedback circuit and eliminating the need for an opto-coupler.

The iW3617 further minimizes the external components count by simplifying the EMI filter with iWatt's EZ-EMI® technology, which eliminates the need for a high-power bleeder. Additionally, the digital control loop of the iW3617 maintains stable overall operating conditions without the need for loop compensation components.

3.0 Applications

- Dimmable LED retrofit lamps up to 25W
- Dimmable LED ballast and luminaires up to 25W



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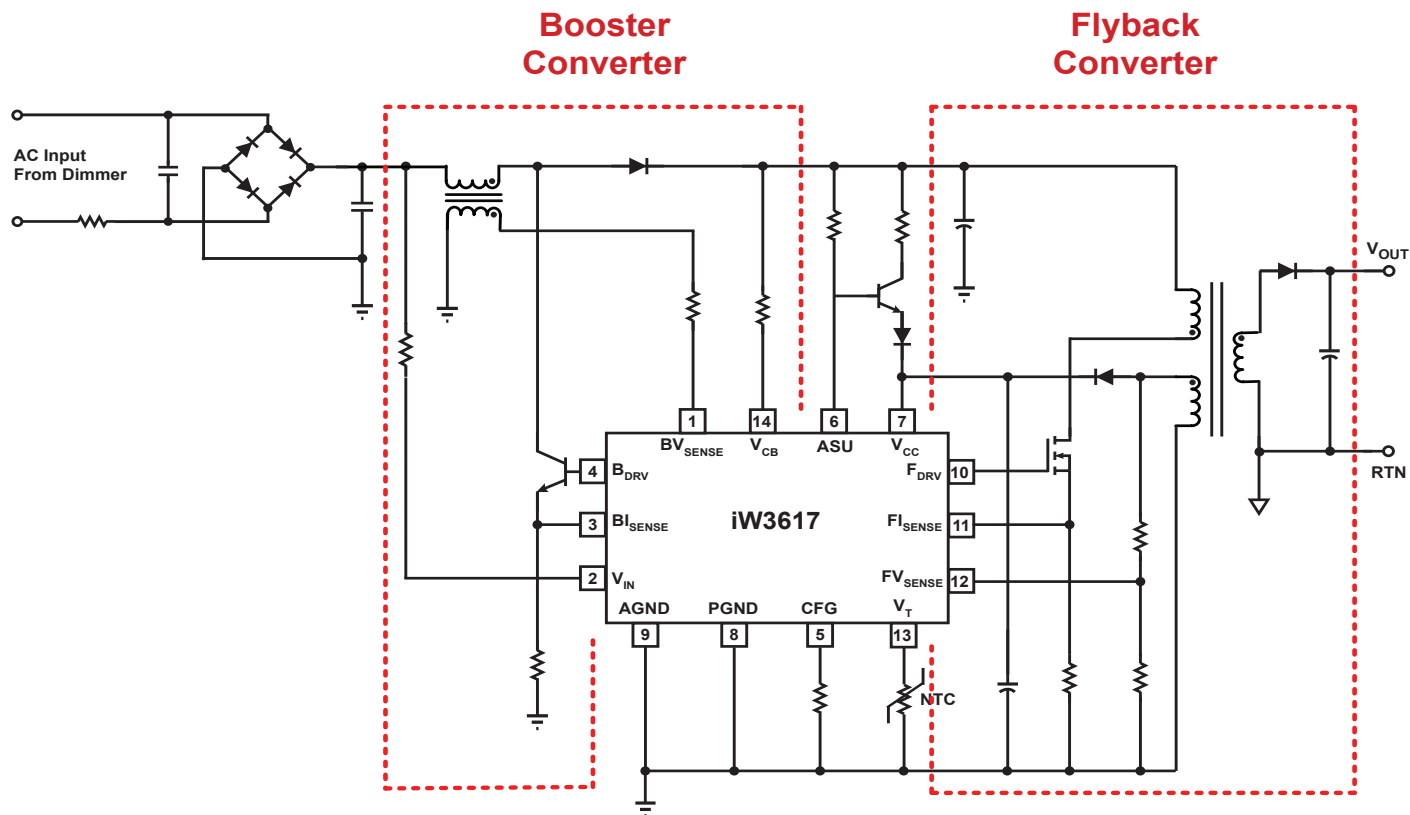


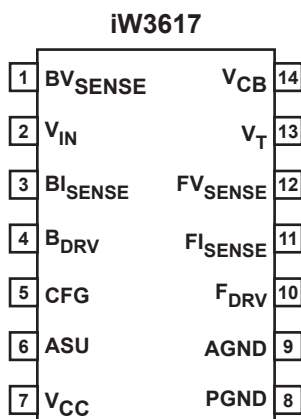
Figure 3.1 : iW3617 Simplified Schematic

iW3617 Product Brief

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4.0 Pinout Description



Pin #	Name	Type	Pin Description
1	BV _{SENSE}	Analog Input	Boost inductor voltage feedback input
2	V _{IN}	Analog Input	Rectified AC line voltage input
3	BI _{SENSE}	Analog Input	Boost current sense input
4	B _{DRV}	Output	Base drive output for boost BJT
5	CFG	Analog In/Out	Driver parameter configuration pin and auxiliary driver
6	ASU	Output	Active start-up and bleeder control
7	V _{CC}	Power	Power supply for control logic and voltage sense for power-on reset circuit
8	PGND	Ground	Power ground
9	AGND	Ground	Signal ground. It should be connected to the power ground on PCB.
10	F _{DRV}	Output	Gate drive output for flyback MOSFET
11	FI _{SENSE}	Analog Input	Flyback current sense (used for cycle-by-cycle peak current control and limit)
12	FV _{SENSE}	Analog Input	Flyback voltage sense (used for primary-side regulation and ZVS)
13	V _T	Analog Input	External power limit shutdown control and external over-temperature power derating
14	V _{CB}	Analog Input	Boost output voltage feedback input

iW3617 Product Brief

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5.0 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded. For maximum safe operating conditions, refer to iW3617 Datasheet for more information.

Parameter	Symbol	Value	Units
DC supply voltage range (pin 7, $I_{CC} = 20\text{mA max}$)	V_{CC}	-0.3 to 18	V
DC supply current at V_{CC} pin	I_{CC}	20	mA
F_{DRV} output (pin 10)		-0.3 to 18	V
B_{DRV} output (pin 4)		-0.3 to 4.0	V
CFG input (pin 5)		-0.3 to 4.0	V
CFG output (pin 5)		-0.3 to 18	V
FV_{SENSE} input (pin 12, $I \leq 10\text{mA}$)		-0.7 to 4.0	V
BV_{SENSE} input (pin 1, $I \leq 3\text{mA}$)		-0.7 to 4.0	V
V_{IN} input (pin 2)		-0.3 to 18	V
V_{CB} input (pin 14)		-0.3 to 18	V
FI_{SENSE} input (pin 11)		-0.3 to 4.0	V
BI_{SENSE} input (pin 3)		-0.3 to 4.0	V
ASU output (pin 6)		-0.3 to 18	V
V_T input (pin 13)		-0.3 to 4.0	V
Power dissipation at $T_A \leq 25^\circ\text{C}$		900	mW
Maximum junction temperature	T_{JMAX}	150	$^\circ\text{C}$
Storage temperature	T_{STG}	-65 to 150	$^\circ\text{C}$
Lead temperature during IR reflow for ≤ 15 seconds	T_{LEAD}	260	$^\circ\text{C}$
Thermal Resistance Junction-to-PCB Board Surface Temperature	ψ_{JB} (Note 1)	45	$^\circ\text{C/W}$
ESD rating per JEDEC JESD22-A114		2,000	V
Latch-up test per JEDEC 78		± 100	mA

Notes:

Note 1. ψ_{JB} [Psi Junction to Board] provides an estimation of the die junction temperature relative to the PCB surface temperature. This data is measured at the ground pin (pin 8 and pin 9) without using any thermal adhesives.

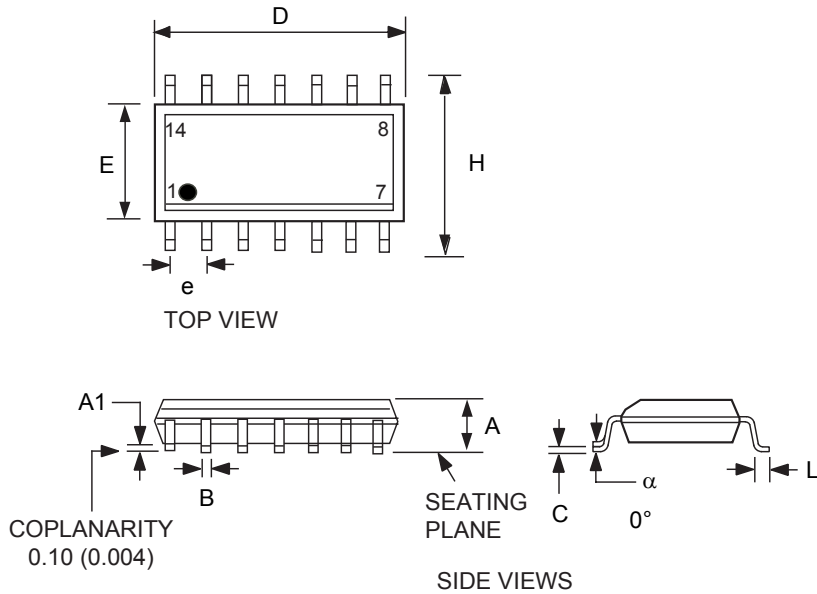
iW3617 Product Brief

AC/DC Digital Power Controller for High Power Factor Dimmable LED Drivers



6.0 Physical Dimensions

14-Lead SOIC Package



Symbol	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.013	0.020	0.33	0.51
C	0.007	0.010	0.19	0.25
D	0.337	0.344	8.55	8.75
E	0.150	0.157	3.80	4.00
e	0.050 BSC		1.27 BSC	
H	0.228	0.244	5.80	6.20
N	0.086	0.094	2.18	2.39
M	0.118	0.126	3.00	3.20
L	0.016	0.050	0.40	1.27
α	0°	8°	0°	8°

Figure 6.1 : Physical dimensions, 14-lead SOIC package

Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:

[a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1

[b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; package can withstand 10 s immersion < 270°C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25 mm per side.

7.0 Ordering Information

Part Number	Options	Package	Description
iW3617-00	120V _{AC} Input	SOIC-14	Tape & Reel ¹
iW3617-01	230V _{AC} Input	SOIC-14	Tape & Reel ¹

Note 1: Tape & Reel packing quantity is 2,500/reel.

iW3617 Product Brief

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