

Transient Voltage Suppressors for ESD Protection

General Description

The LESD5Z5.0CT1G is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

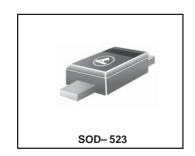
Features

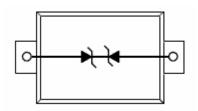
- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 200 Watts @ 8 x 20 _s
 Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection
- S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

Absolute Ratings (T_{amb}=25°C)

Symbol	Parameter	Value	Units
P _D	Total power dissipation@Ta=25°C	150	mW
	Derate above 25°C	1.2	mW/°C
R_{JA}	Thermal Resistance, Junction to Ambient	833	°C/W
T_L	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +155	°C
T_{op}	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air dischar contact dischar		KV
	IEC61000-4-4 (EFT)	40	А
	ESD Voltage Per Human Body Mod	del 16	KV

LESD5Z5.0CT1G S-LESD5Z5.0CT1G





ORDERING INFORMATION

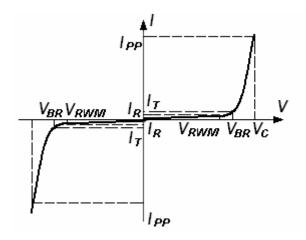
Device	Marking	Shipping		
LESD5Z5.0CT1G	5C	3000/Tape & Reel		



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Electrical Parameter

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V_{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
I _T	Test Current
V_{BR}	Breakdown Voltage @ I _T



Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

Device	V _{RWM} (V)	I _R (uA) @ V _{RWM}		/)@ I _T te 1)	Ι _τ	V _C (V) @ I _{PP} =5 A*	V _C (V) @ Max I _{PP} *	I _{PP} (A)*	P _{PK} (W)*	C (pF)
	Max	Max	Min	Max	mA	Тур	Max	Max	Max	Тур
LESD5Z5.0CT1G	5.0	1	5.6	7.8	1.0	11.6	18.6	9.4	174	25

^{*}Surge current waveform per Figure 1.

1. V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25 $^\circ\!\!\!\!\!\!\!^\circ$.

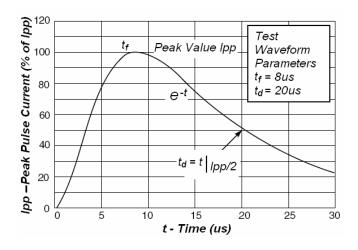
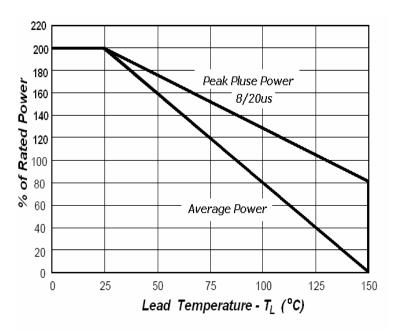
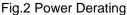


Fig1. Pulse Waveform



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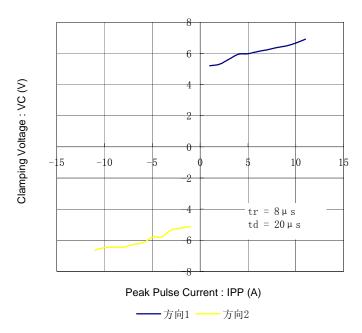


Fig.3 Normalized Junction Capacitance
Voltage vs.Reverse Voltage

Application Note

Electrostatic discharge (ESD) is a major cause of failure in electronic systems. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented.

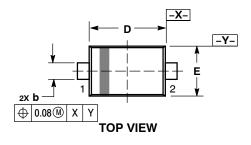
Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rises above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The LESD5Z5.0CT1G is the ideal board evel protection of ESD sensitive semiconductor components.

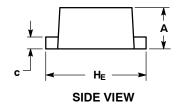
The tiny SOD-523 package allows design flexibility in the design of high density boards where the space saving is at a premium. This enables to shorten the routing and contributes to hardening against ESD.



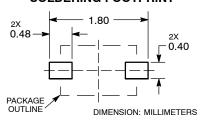
LESD5Z5.0CT1G,S-LESD5Z5.0CT1G

SC-79/SOD-523





RECOMMENDED SOLDERING FOOTPRINT*



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF
 BASE MATERIAL.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS							
DIM	MIN	NOM	MAX					
Α	0.50	0.60	0.70					
b	0.25	0.30	0.35					
С	0.07	0.14	0.20					
D	1.10	1.20	1.30					
E	0.70	0.80	0.90					
HE	1.50	1.60	1.70					
L	0.30 REF							
12	12 0.15 0.5		0.25					