

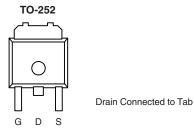
N-Channel 100-V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
100	0.025 at V _{GS} = 10 V	40		
100	0.028 at V _{GS} = 4.5 V	38		

FEATURES

- TrenchFET[®] Power MOSFET
- 175 °C Maximum Junction Temperature
- 100 % Rg Tested





Top View

Ordering Information: SUD40N10-25 SUD40N10-25-E3 (Lead (Pb)-free)

G

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	- V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C	1	40	-	
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 125 °C	I _D	23		
Pulsed Drain Current	I _{DM}	70	А		
Continuous Source Current (Diode Conduction)		۱ _S	40		
Avalanche Current		I _{AS}	40		
Single Pulse Avalanche Energy (Duty Cycle \leq 1 %)	L = 0.1 mH	E _{AS}	80	mJ	
Manimum Davier Disaination	T _C = 25 °C	Р	136 ^b		
Maximum Power Dissipation	T _A = 25 °C	P _D -	3 ^a	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA}	15	18	°C/W
Junction-to-Ambient ^a	Steady State		40	50	
Junction-to-Case		R _{thJC}	0.85	1.1	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SUD40N10-25

Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	100			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		3.0	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		V _{DS} = 100 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA	
		V_{DS} = 100 V, V_{GS} = 0 V, T_{J} = 175 °C			250		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	70			А	
		V _{GS} = 10 V, I _D = 40 A		0.02	0.025	- Ω	
	D	V_{GS} = 10 V, I _D = 40 A, T _J = 125 °C			0.05		
Drain-Source On-State Resistance ^b	R _{DS(on)}	V_{GS} = 10 V, I _D = 40 A, T _J = 175 °C			0.063		
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.022	0.028		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 40 A		70		S	
Dynamic ^a							
Input Capacitance	C _{iss}			2400		pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, F = 1 MHz		290			
Reverse Transfer Capacitance	C _{rss}			120			
Total Gate Charge ^c	Qg			40	60		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 40 \text{ A}$		11		nC	
Gate-Drain Charge ^c	Q _{gd}			9		1	
Gate Resistance	R _g		1		3.5	Ω	
Turn-On Delay Time ^c	t _{d(on)}			8	13		
Rise Time ^c	t _r	V_{DD} = 50 V, R_L = 1.25 Ω		40	60		
Turn-Off Delay Time ^c	t _{d(off)}	$\text{I}_\text{D}\cong$ 40 A, V_GEN = 10 V, R_g = 2.5 Ω		15	25	ns -	
Fall Time ^c	t _f			80	120		
Source-Drain Diode Ratings and Cha	racteristics 7	Γ _C = 25 °C		·	· ·		
Pulsed Current	I _{SM}				70	А	
Diode Forward Voltage ^b	V _{SD}	I _F = 40 A, V _{GS} = 0 V		1.0	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 40 A, dl/dt = 100 A/μs		75	120	ns	

Notes:

a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

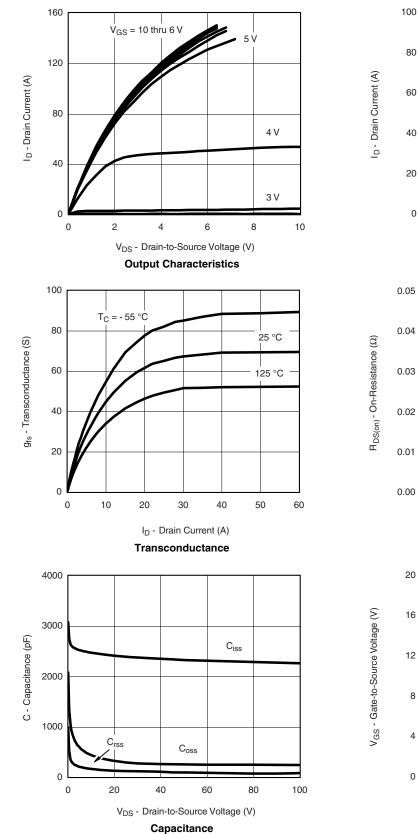
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



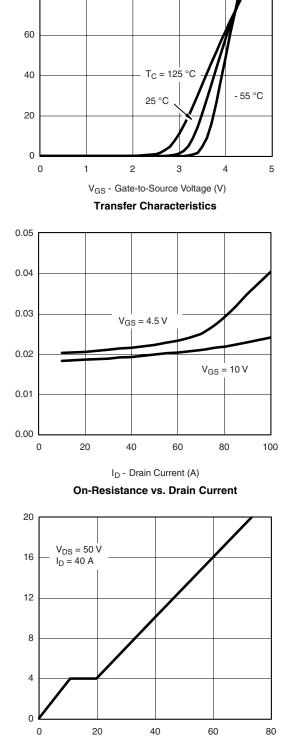
SUD40N10-25

Vishay Siliconix



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Document Number: 71140 S-81732-Rev. E, 04-Aug-08

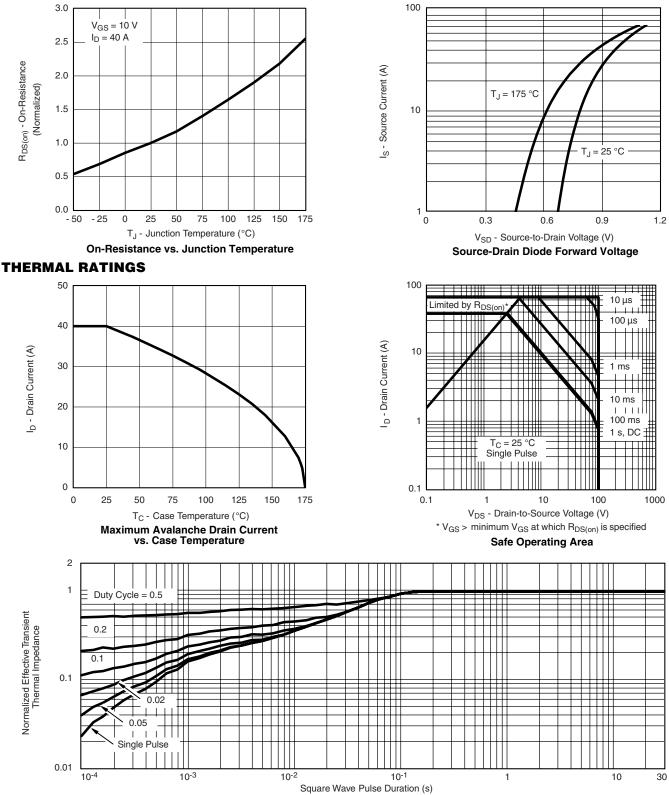


Qg - Total Gate Charge (nC)

Gate Charge

80

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71140.

/ISHA





TO-252AA Case Outline

VERSION 1: FACILITY CODE = Y







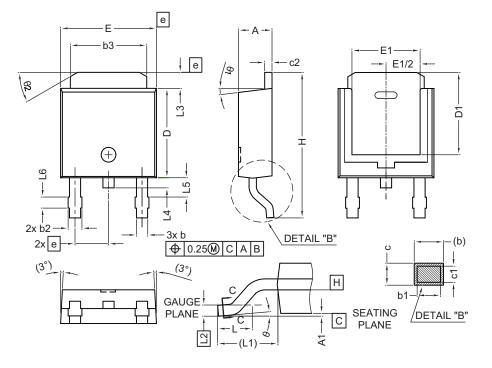
	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
E	6.35	6.73	
E1	4.32	-	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

Note

• Dimension L3 is for reference only



VERSION 2: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
A	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
E	6.35	6.73	
E1	4.32	-	
e	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74 ref.		
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

Notes

• Dimensioning and tolerance confirm to ASME Y14.5M-1994

• All dimensions are in millimeters. Angles are in degrees

• Heat sink side flash is max. 0.8 mm

Radius on terminal is optional

ECN: E19-0649-Rev. Q, 16-Dec-2019 DWG: 5347



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.