

## Vishay Semiconductors

# **Small Signal Schottky Diodes**



### **MECHANICAL DATA**

Case: QuadroMELF SOD-80
Weight: approx. 34 mg
Cathode band color: black
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/2.5K per 7" reel (8 mm tape), 12.5K/box

#### **FEATURES**

Integrated protection ring against static discharge



• Low capacitance

Low leakage current

Low forward voltage drop

RoHS COMPLIAN

- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **APPLICATIONS**

- HF-detector
- Protection circuit
- · Small battery charger
- AC-DC/DC-DC converter for notebooks

PARTS TABLE						
PART	TYPE DIFFERENTATION	NTATION ORDERING CODE INTERNAL CONSTRUCTION		REMARKS		
LS103A	V <sub>R</sub> = 40 V	LS103A-GS18 or LS103A-GS08	Single diode	Tape and reel		
LS103B	V <sub>R</sub> = 30 V	LS103B-GS18 or LS103B-GS08	Single diode	Tape and reel		
LS103C	V <sub>R</sub> = 20 V	LS103C-GS18 or LS103C-GS08	Single diode	Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
		LS103A	$V_R$	40	V	
Reverse voltage		LS103B	$V_R$	30	V	
		LS103C	$V_R$	20	V	
Peak forward surge current	t <sub>p</sub> = 300 μs, square pulse		I <sub>FSM</sub>	15	Α	
Power dissipation			P <sub>tot</sub>	400	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	On PC board 50 mm x 50 mm x 1.6 mm	R <sub>thJA</sub>	250	K/W		
Junction temperature		Tj	125	°C		
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C		

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 10 μA	LS103A	V <sub>(BR)</sub>	40			V
		LS103B	V <sub>(BR)</sub>	30			V
		LS103C	V <sub>(BR)</sub>	20			V
Leakage current	V <sub>R</sub> = 30 V	LS103A	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 20 V	LS103B	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 10 V	LS103C	I <sub>R</sub>			5	μΑ
Forward voltage drop	I <sub>F</sub> = 20 mA		V <sub>F</sub>			370	mV
	I <sub>F</sub> = 200 mA		V <sub>F</sub>			600	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>D</sub>		50		рF
Reverse recovery time	$I_F = I_R = 50$ mA to 200 mA, recover to 0.1 $I_R$		t <sub>rr</sub>		10		ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

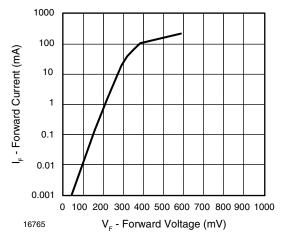


Fig. 1 - Forward Current vs. Forward Voltage

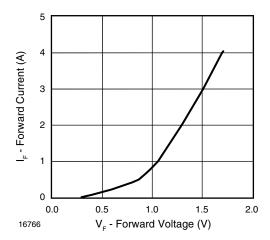


Fig. 2 - Forward Current vs. Forward Voltage

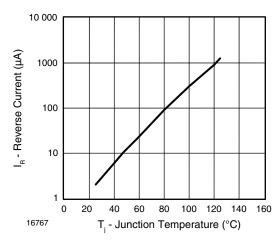


Fig. 3 - Reverse Current vs. Junction Temperature

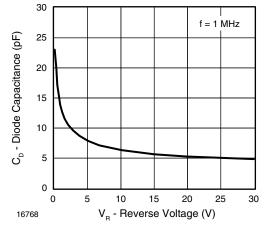


Fig. 4 - Diode Capacitance vs. Reverse Voltage

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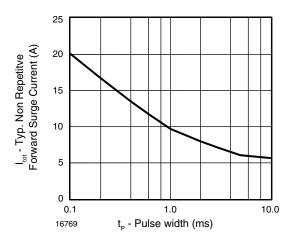
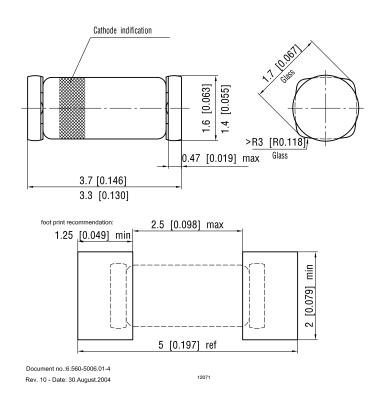


Fig. 5 - Typical Non-Repetitive Forward Surge Current vs. Pulse Width

### PACKAGE DIMENSIONS in millimeters (inches): QuadroMELF SOD-80





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