

Chip tantalum capacitors

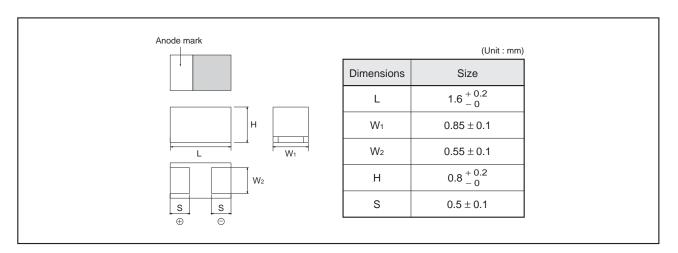
(New bottom surface electrode type: Extra large capacitance)

TCS Series M Case Datasheet

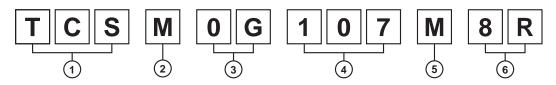
Features

- 1) New bottom electrode configuration results in greater compactness, low profile, and higher capacitance.
- 2) Compact, low profile, ultra-high capacitance contribute to smaller, thinner sets with greater functionality.
- 3) Ideal for noise removal on power supply lines with limited space.
- 4) Eco-friendly halogen-free products.

Dimensions



●Part No. Explanation



1 Series name

TCS

2 Case style

M: 1608-10 (0603) size

3 Rated voltage

Rated voltage (V)	4	6.3	10	16
CODE	0G	0J	1A	1C

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

M: ±20%

(6) Taping

8 : Reel width : 8mm

R : Positive electrode on the side opposite to sprocket hole

^{*}This specification has possibility of charge, due to underdevelopment product. Please ask for latest specification to our sales.

TCS Series M Case Datasheet

●Rated table

Capacitance	Rated voltage (V.DC)								
(μF)	4	6.3	10	16					
10 (106)				М					
15 (156)			☆M						
22 (226)			M						
33 (336)		☆M							
47 (476)		М							
100 (107)	М								

Remark) Case size codes (M) in the above show products line-up.

☆ Under development

Marking

The indications listed below should be given on the surface of a capacitor.

(1) Polarity : The polarity should be shown by □ bar. (on the anode side)

(2) Rated DC voltage: A voltage code is shown as below table.

(3) Capacitance : A capacitance code is shown as below table.

Voltage Code	Rated DC Voltage (V)
g	4
j	6.3
А	10
С	16

Capacitance Code	Nominal Capacitance (μF)
а	10
е	15
j	22
n	33
S	47
ā	100

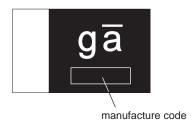
Visual typical example

voltage code and capacitance code are variable with parts number.

[M case]

$$\frac{g}{(1)}$$
 $\frac{a}{(2)}$

(1) voltage code (2) capacitance code



TCS Series M Case

● Characteristics

Iter	Performance					formance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)						
Operating Temperature			-55°C to +125°C						Voltage reduction when temperature exceeds +85°C				
Maximum opera temperature wit derating	ating h no voltage	+8	5°C										
Rated voltage (V.DC)	2.5	4	6.3	10	16		at 85°C					
Category voltag	e (V.DC)	1.6 2.5 4 6.3 10						at 12	5°C				
Surge voltage (3.2 5 8 13 20						at 85°	°C				
DC Leakage cu	rrent	Shall be satisfied the value on " Standard list "					ne value on	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage: Rated voltage for 5min					
Capacitance tolerance			Shall be satisfied allowance range. ±20%				llowance range.	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC Equivalent series circuit					
Tangent of loss angle (Df, $\tan \delta$)			Shall be satisfied the value on " Standard list "					As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5V.DC Measuring circuit : DC Equivalent series circuit					
Impedance / ESR			Shall be satisfied the value on " Standard list "					As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage: 0.5Vrms or less Measuring circuit: DC Equivalent series circuit					
Resistance to Soldering heat		There should be no significant abnormality. The indications should be clear.					9	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3					
	L.C.	Less than 200% of initial limit					initial limit	Dip in the solder bath Solder temp : 260±10°C					
	⊿C/C	Within ±30% of initial value						Duration : 5±0.5s Repetition : 1					
	Df (tan δ)	Less than 200% of initial limit							After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.						As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3					
	L.C.	Less than 200% of initial limit					initial limit	Repetition: 5 cycles (1 cycle: steps 1 to 4) without discontinuation.					
	⊿C / C	Wi	thin	±30	% o	f init	ial value] ` [Temp. Time			
	Df (tan δ)	Les	ss th	nan :	200°	% of	initial limit	1 1	1	-55±3°C 30±3min.			
	(-2.7. 0)		"						2	Room temp. 3min. or less			
									3	125±2°C 30±3min.			
									4	Room temp. 3min. or less			
					After the specimens, leave it at room temperature for over 24h and then measure the sample.								
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.						As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3					
	L.C.	Less than 200% of initial limit					initial limit	After leaving the sample under such atmospheric condition that the temperature and humidity are 60±2°C and 90 to 95% RH, respectively, for 500±12h leave it at room temperature for over 24h and then measure					
	⊿C/C	Within ±30% of initial value				ial value							
	2070	1			Less than 200% of initial limit								

Iten	1	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)				
Temperature Stability	Temp.	−55°C	As per 4.29 JIS C 5101-1 - As per 4.13 JIS C 5101-3				
Stability	⊿C/C	Within 0/–30% of initial value					
	Df (tan δ)	Shall be satisfied the value on " Standard list "					
	L.C.	_					
	Temp.	+85°C					
	⊿C / C	Within +15/-5% of initial value					
	Df (tan δ)	Shall be satisfied the value on " Standard list "					
	L.C.	Less than 1000% of initial limit					
	Temp.	+125°C					
	⊿C / C	Within +20/–5% of initial value					
	Df (tan δ)	Shall be satisfied the value on " Standard list "					
	L.C.	Less than 1250% of initial limit					
Surge voltage	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.26JIS C 5101-1 As per 4.14JIS C 5101-3				
	L.C.	Less than 200% of initial limit	Apply the specified surge voltage via the serial resistance of 1kΩ every 5±0.5 min for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for				
	<u>⊿</u> C / C	Within ±30% of initial value					
	Df (tan δ)	Less than 200% of initial value					
	Di (tali 0)	Less than 200% of Initial value	over 24h and then measure the sample.				
Loading at High temperature	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3 After applying the rated voltage for 1000+36/0 h without discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave the sample at room				
	L.C.	Less than 200% of initial limit					
		Within ±30% of initial value					
	Df (tan δ)	Less than 200% of initial limit	temperature / humidity for over 24h and measure the value.				
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure on the right)				
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.				
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.				
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.				
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed: 25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration: 3±0.5s Solder: M705 Flux: Rosin 25% IPA 75%				
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency: 10 to 55 to 10Hz/min. Amplitude: 1.5mm Time: 2h each in X and Y directions Mounting: The terminal is soldered on a print circuit board.				
		There should be no significant abnormality.					

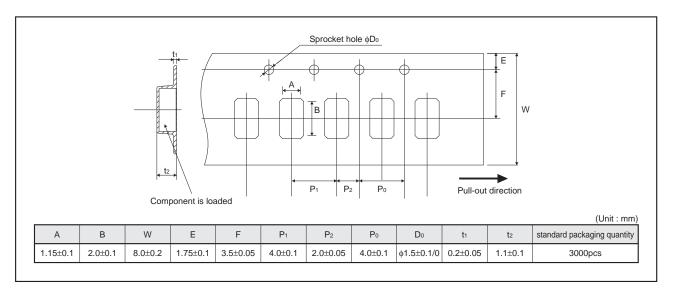
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●Standard products list

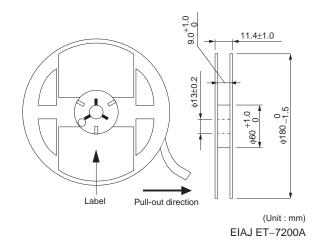
Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		ESR 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.5min (μA)	–55°C	25°C 85°C	125°C	(Ω)
TCS M 0G 107 M8R	4	2.5	5	100	± 20	80	80	40	60	4.0
* TCS M 0J 336 M8R	6.3	4	8	33	± 20	20.8	60	30	40	4.0
TCS M 0J 476 M8R	6.3	4	8	47	± 20	29.7	80	40	60	4.0
* TCS M 1A 156 M8R	10	6.3	13	15	± 20	7.5	30	20	30	6.0
TCS M 1A 226 M8R	10	6.3	13	22	± 20	11.0	60	30	40	5.0
TCS M 1C 106 M8R	16	10	20	10	± 20	8.0	30	20	30	6.0

^{* =} Under development

Packaging specifications



Reel dimensions



Notes

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