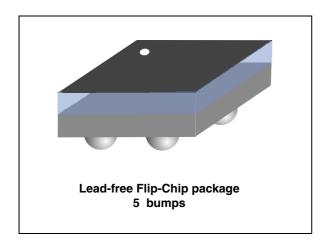
### **BALF-NRF01D3**



# $50~\Omega$ nominal input / conjugate match balun to nRF51822-QFAAG0 /QFABB0 and nRF51422-QFAAE0 with integrated harmonic filter

Datasheet - production data



#### **Features**

- Low insertion loss
- Low amplitude imbalance
- · Low phase imbalance
- · Coated Flip-Chip on Glass
- Small footprint: < 1.5 mm<sup>2</sup>

#### **Benefits**

- Very low profile: < 560 µm after reflow</li>
- · High RF performance
- PCB space saving versus discrete solution
- BOM count reduction
- Efficient manufacturability

#### **Applications**

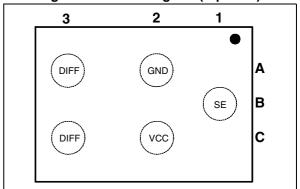
- 2.45 GHz balun with integrated matching network
- Matching optimized for following chipsets: nRF51822-QFAAG0/GC/FA, nRF51822-QFABB0 and nRF51422-QFAAE0

#### **Description**

STMicroelectronics BALF-NRF01D3 is an ultraminiature balun. The BALF-NRF01D3 integrates matching network in a monolithic glass substrate. Matching impedance has been customized for the nRF51822-QFAAG0/GC/FA, nRF51822-QFABB0 and nRF51422-QFAAE0 RF transceivers.

The BALF-NRF01D3 uses STMicroelectronics IPD technology on non-conductive glass substrate which optimize RF performances.

Figure 1. Pinout diagram (top view)



Characteristics BALF-NRF01D3

## 1 Characteristics

**Table 1. Absolute maximum ratings (limiting values)** 

Symbol	Parameter		Value		
			Тур.	Max.	Unit
P <sub>IN</sub>	Input Power RF <sub>IN</sub>			20	dBm
	ESD ratings MIL STD883C (HBM: C = 100 pF, R = 1.5 k $\Omega$ , air discharge)	2000			
V <sub>ESD</sub>	ESD ratings charge device model (JESD22-C101-C)	500			V
	ESD ratings machine model (MM: C = 200 pF, R = 25 $\Omega$ , L = 500 nH)	500			
T <sub>OP</sub>	Operating temperature	-40		+85	°C

Table 2. Electrical characteristics( $T_{amb} = 25 \text{ °C}$ )

Symbol	Parameter	Value			
Symbol	raiametei	Min.	Тур.	Max.	Unit
Z <sub>OUT</sub>	Nominal differential output impedance		conjugate match to:  – nRF51822-QFAAG0/GC/FA  – nRF51822-QFABB0  – nRF51422-QFAAE0		Ω
Z <sub>IN</sub>	Nominal input impedance		50		Ω
F	Frequency range (bandwidth)	2400		2540	
ΙL	Insertion loss in bandwidth		1.35	1.46	dB
$R_L$	Return loss in bandwidth	16.5	17	17.5	dB
фimb	Phase imbalance	4.5	5	5.5	0
Aimb	Amplitude imbalance	0.15	0.2	0.25	dB
2f0	2nd harmonic filtering		-15	-14	dB
3f0	3rd harmonic filtering		-42	-41	dB

BALF-NRF01D3 Characteristics

## 1.1 Simulations results (T<sub>amb</sub> = 25 °C)

Figure 2. Insertion loss in band

Figure 3. Differential transmission

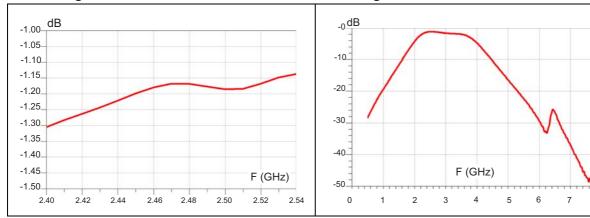


Figure 4. Return loss on SE port

Figure 5. Amplitude imbalance

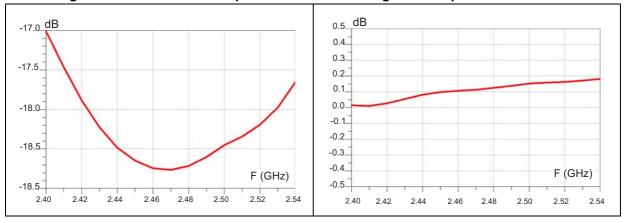
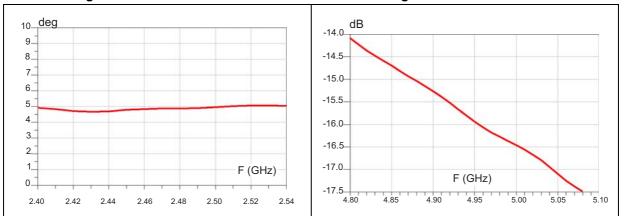


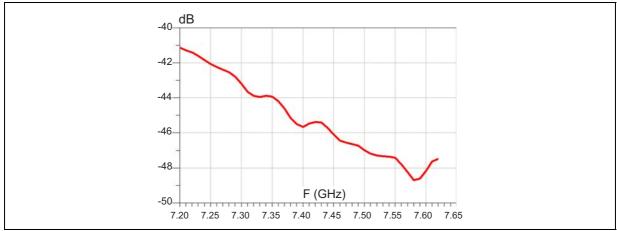
Figure 6. Phase imbalance

Figure 7. H2 attenuation



Characteristics BALF-NRF01D3

Figure 8. Attenuation in H3



# 2 Application information

Please note that the capacitance on SE port (pin 5) is no longer needed.

CS POINT PRODUCT OF POINT PRODUCT

Figure 9. Application schematic

Package information BALF-NRF01D3

## 3 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

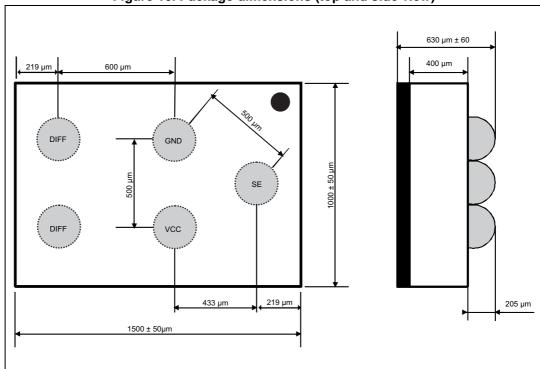
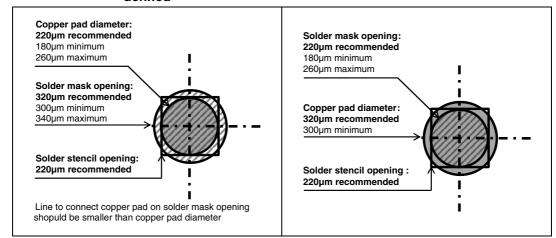


Figure 10. Package dimensions (top and side view)

Figure 11. Footprint - non solder mask Figure 12. Footprint - solder mask defined defined



BALF-NRF01D3 Package information

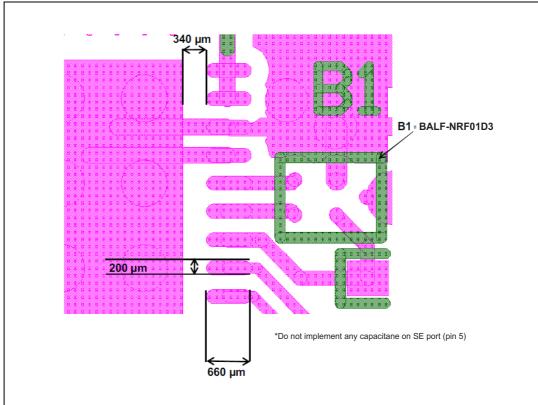
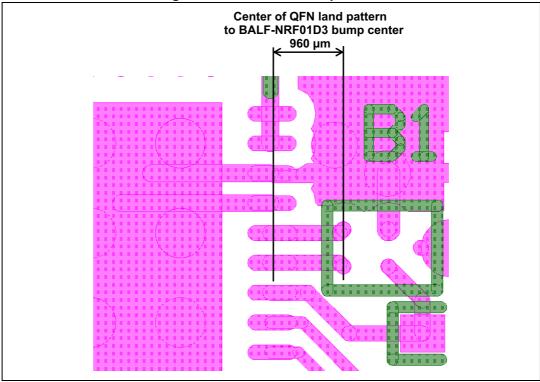


Figure 13. PCB layout recommendation





Package information BALF-NRF01D3

Figure 15. Marking

Dot, ST logo
ECOPACK grade
xx = marking
z = manufacturing
location
yww = datecode

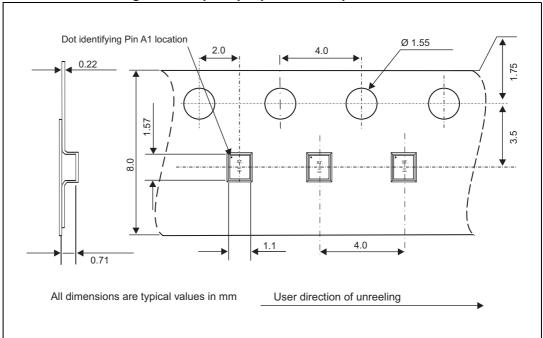
XX X

Y

W

W

Figure 16. Flip-Chip tape and reel specifications



Note: More information is available in the STMicroelectronics Application note:

AN2348 Flip-Chip: "Package description and recommendations for use"

BALF-NRF01D3 Package information

Table 3. Compatibility matrix

Device marking		Palun variant and design files	
Packet/variant	Build code	- Balun variant and design files	
	CA	BAL-NRF01D3 + 0.8pF DF-ST V1.0	
QFAA	C0	BAL-NRF01D3 + 0.8pF DF-ST V1.0	
	FA	BALF-NRF01D3 DF-ST V1.x	
	GC	BAL <b>F</b> -NRF01D3 DF-ST V1.x	
	G0	BAL <b>F</b> -NRF01D3 DF-ST V1.x	
	AA	BAL-NRF01D3 + 0.8pF DF-ST V1.0	
QFAB	A0	BAL-NRF01D3 + 0.8pF DF-ST V1.0	
	В0	BAL <b>F</b> -NRF01D3 DF-ST V1.x	

DF-ST = nRF51822 Reference Layout files with STMicroelectronics balun.

Table 4. Compatibility matrix (nRF51422))

Device marking		Polyn variont and design files		
Packet/variant	Build code	- Balun variant and design files		
	C0	BAL-NRF01D3 + 0.8pF		
QFAA	CA	BAL-NRF01D3 + 0.8pF		
	E0	BALF-NRF01D3		
QFAB A0		BALF-NRF01D3		

Ordering information BALF-NRF01D3

# 4 Ordering information

**Table 5. Ordering information** 

Order code	Marking	Weight	Base Qty	Delivery mode
BALF-NRF01D3	ST	1.82 mg	5000	Tape and Reel

# 5 Revision history

**Table 6. Document revision history** 

Date	Revision	Changes	
27-Mar-2014	1	Initial release	
04-Jun-2014	2	Updated all curves and added Table 4.	

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