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- Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.
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### **MULTILAYER CERAMIC CAPACITORS**



WAVE

REFLOW

### ■PARTS NUMBER

J M K 3 1 6 Δ B J 1 0 6 M L — Τ Δ ① 2 3 4 5 6 7 8 9 ⑩ ① ①

△=Blank space

①Rated voltage

Code	Rated voltage[VDC]
Р	2.5
Α	4
J	6.3
L	10
E	16
Т	25
G	35
U	50
Н	100
Q	250
S	630

Code	End termination
K	Plated
S	Cu Internal Electrodes

①Dimension(L×W)

 $\ensuremath{\mathfrak{G}}$ End termination

Type	Dimensions (L×W)[mm]	EIA (inch)
021	0.25 × 0.125	008004
042	0.4 × 0.2	01005
063	0.6 × 0.3	0201
105	1.0 × 0.5	0402
105	0.52 × 1.0 ※	0204
107	1.6 × 0.8	0603
107	0.8 × 1.6 ※	0306
010	2.0 × 1.25	0805
212	1.25 × 2.0 ※	0508
316	3.2 × 1.6	1206
325	3.2 × 2.5	1210
432	4.5 × 3.2	1812
N-+ VIW	(DMIZ)	

Note: ※LW reverse type(□WK) only

### ②Series name

Series name
Multilayer ceramic capacitor
Multilayer ceramic capacitor for high frequency
LW reverse type multilayer capacitor

⑤Dimension tolerance

Code	Туре	L[mm]	W[mm]	T[mm]
Δ	ALL	Standard	Standard	Standard
	063	0.6±0.05	0.3±0.05	0.3±0.05
	105	1.0±0.10	0.5±0.10	0.5±0.10
	107	1.6+0.15/-0.05	0.8+0.15/-0.05	0.8+0.15/-0.05
				0.45±0.05
Α	212	2.0+0.15/-0.05	1.25 + 0.15 / -0.05	0.85±0.10
				1.25 + 0.15 / -0.05
	010	0.01.000	10100	0.85±0.10
	316	3.2±0.20	1.6±0.20	1.6±0.20
	325	3.2±0.30	2.5±0.30	2.5±0.30
	063	0.6±0.09	0.3±0.09	0.3±0.09
	105	1.0+0.15/-0.05	0.5+0.15/-0.05	0.5+0.15/-0.05
	107	101000/ 0	0.0.1.0.00/	0.45±0.05
В	107	1.6+0.20/-0	0.8+0.20/-0	0.8+0.20/-0
В				0.45±0.05
212 316	212	2 2.0+0.20/-0	1.25+0.20/-0	0.85±0.10
				1.25+0.20/-0
	316	3.2±0.30	1.6±0.30	1.6±0.30
С	105	1.0+0.20/-0	0.5+0.20/-0	0.5+0.20/-0

Note: P.6 Standard external dimensions

△= Blank space

### **6**Temperature characteristics code

■ High dielectric type (Excluding Super low distortion multilayer ceramic capacitor(CFCAP™))

Code		cable dard	Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code
	JIS	В	-25~+ 85	20	±10%	±10%	K
BJ	010	В	23.4 1 83	20	± 10 70	±20%	М
БО	EIA	X5R	-55 <b>~</b> + 85	25	±15%	±10%	К
	EIA	YOK	-557-4-65	25	工13%	±20%	М
В7	EIA	X7R	-55~+125	25	±15%	±10%	K
Б/	B/ EIA X/R	Λ/Κ	-55~+125	25	<u> </u>	±20%	М
C6	EIA	X6S	-55~+105	25	±22%	±10%	K
CO	EIA	703	-55/	25	1 22 90	±20%	М
C7	EIA	X7S	-55~+125	25	±22%	±10%	К
67	EIA	X/S	-55~+125	25	±22%	±20%	М
. 5()(()		V	55   05	0.5		±10%	K
LD(※) EIA X	X5R	X5R	25	25 ±15%	±20%	М	
	JIS	F	-25 <b>~</b> + 85	20	+30/-80%	+80/-20%	Z
ΔF	EIA	Y5V	-30 <b>~</b> + 85	25	+22/-82%	+80/-20%	Z

Note : &.LD Low distortion high value multilayer ceramic capacitor

∆= Blank space

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Tam	perature	compar	eating	tyna
i eiii	perature	Comper	isaurig	LVDE

Code		icable idard	Temperature range [°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code		
						±0.05pF	Α		
						±0.1pF	В		
CG	EIA	C0G	<b>−55∼+125</b>	25	$0\pm30$ ppm/°C	±0.25pF	С		
						±0.5pF	D		
						±5%	J		
						±0.05pF	Α		
	JIS	CH		20		±0.1pF	В		
CH			<b>−55~+125</b>		$0\pm60$ ppm/°C	±0.25pF	С		
	EIA	COL		25		±0.5pF	D		
	EIA C0H		20		±5%	J			
CJ	JIS	CJ	-55~+125	20	0+100/00	±0.1pF	В		
CJ	EIA	C0J	—55 <b>~</b> + 125	25	0±120ppm/°C	±0.25pF	С		
	150	OK		20		±0.05pF	Α		
CK	JIS	CK	-55 <b>~</b> +125	20	$0\pm250$ ppm/°C	±0.1pF	В		
	EIA	C0K		25		±0.25pF	С		
	JIS					20		±0.25pF	С
UJ	318	UJ	<b>−55∼+125</b>	20	$-750\pm120$ ppm/°C	±0.5pF	D		
	EIA	U2J		25		±5%	J		
UK	JIS	UK	-55 <b>~</b> +125	20	_750±250/°C	±0.25-F	С		
UN	EIA	U2K	-55~+125	25	$-750\pm250$ ppm/°C	±0.25pF	C		
SL	JIS	SL	-55~+125	20	+350~-1000ppm/°C	±5%	J		

### 6 Series code

(Super low distortion multilaver ceramic capacitor(CFCAP™) only)

Code	Series code
SD	Standard

• Medium-High Voltage Multilayer Ceramic Capacitors

Code	Series code
SD	Standard

### 7Nominal capacitance

Code (example)	Nominal capacitance
0R5	0.5pF
010	1pF
100	10pF
101	100pF
102	1,000pF
103	10,000pF
104	0.1 μ F
105	1.0 <i>μ</i> F
106	10 μ F
107	100 μ F
N . D D .	

Note: R=Decimal point

### 8 Capacitance tolerance

Code	Capacitance tolerance
Α	±0.05pF
В	±0.1pF
С	±0.25pF
D	±0.5pF
F	±1pF
G	±2%
J	±5%
K	±10%
М	±20%
Z	+80/-20%
•	

### Thickness

Code	Thickness[mm]
K	0.125
Н	0.13
E	0.18
С	0.2
D	0.2
Р	0.3
Т	0.3
К	0.45(107type or more)
V	0.5
W	0.5
Α	0.8
D	0.85(212type or more)
F	1.15
G	1.25
L	1.6
N	1.9
Υ	2.0 max
М	2.5

### **10**Special code

	Code	Special code
_	_	Standard
-		

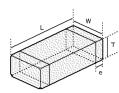
### ①Packaging

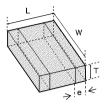
Code	Packaging								
F	φ178mm Taping (2mm pitch)								
Т	φ178mm Taping (4mm pitch)								
Р	\$\phi_178mm Taping (4mm pitch, 1000 pcs/reel)								
Р	325 type (Thickness code M)								
R	φ178mm Taping (2mm pitch)105type only								
K	(Thickness code E,H)								
W	φ178mm Taping(1mm pitch)021/042type only								
	•								

### 12Internal code

G. Incomination and									
Code	Internal code								
Δ	Standard								

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★ LW reverse type

T (FIL)		D	imension [mm]			
Type( EIA )	L	W	T	*1	е	
☐MK021 (008004)	0.25±0.013	0.125±0.013	0.125±0.013	K	0.0675±0.0275	
□MK042(01005)	0.4±0.02	0.2±0.02	0.2±0.02	C D	0.1±0.03	
□VS042(01005)	0.4±0.02	0.2±0.02	0.2±0.02	С	0.1±0.03	
□MK063(0201)	0.6±0.03	0.3±0.03	0.3±0.03	P T	0.15±0.05	
			0.13±0.02	Н		
			0.18±0.02	Е		
☐MK105(0402)	1.0±0.05	$0.5 \pm 0.05$	0.2±0.02	С	0.25±0.10	
			0.3±0.03	Р		
			0.5±0.05	٧		
□VK105(0402)	1.0±0.05	0.5±0.05	0.5±0.05	W	0.25±0.10	
□WK105(0204)※	$0.52 \pm 0.05$	1.0±0.05	0.3±0.05	Р	0.18±0.08	
□MK107(0603)	1.6±0.10	0.8±0.10	0.45±0.05	K	0.35±0.25	
	1.0±0.10	0.8±0.10	0.8±0.10	Α	0.35±0.25	
□WK107(0306)※	0.8±0.10	1.6±0.10	$0.5 \pm 0.05$	>	0.25±0.15	
			$0.45 \pm 0.05$	Κ		
□MK212(0805)	2.0±0.10	1.25±0.10	0.85±0.10	D	$0.5 \pm 0.25$	
			1.25±0.10	G		
□WK212(0508)※	1.25±0.15	2.0±0.15	$0.85 \pm 0.1$	D	0.3±0.2	
			0.85±0.10	D		
□MK316(1206)	3.2±0.15	1.6±0.15	1.15±0.10	F	0.5+0.35/-0.25	
□MK310(1200)	3.2±0.15	1.0±0.15	1.25±0.10	G	0.5 + 0.35/ - 0.25	
			1.6±0.20	L		
			0.85±0.10	D		
			1.15±0.10	F		
□MK325(1210)	3.2±0.30	2.5±0.20	1.9±0.20	N	0.6±0.3	
			1.9+0.1/-0.2	Υ		
			2.5±0.20	М		
□MK432(1812)	4.5±0.40	3.2±0.30	2.5±0.20	М	0.9±0.6	

Note: X. LW reverse type, \*1.Thickness code

### ■STANDARD QUANTITY

<b>-</b>	F14 /: 1 )	Dimer	ision	Standard o	uantity[pcs]	
Туре	EIA (inch)	[mm]	Code	Paper tape	Embossed tape	
021	008004	0.125	К	_	50000	
042	01005	0.2	С		40000	
042	01005	0.2	D	_	40000	
063	0201	0.3	Р	15000	_	
003	0201	0.5	Т	13000		
		0.13	Н	-	20000	
		0.18	E	_	15000	
	0402	0.2	С	20000	_	
105	0402	0.3	Р	15000	_	
		0.5	V			
		0.5	W	10000	_	
	0204 ※	0.30	Р			
	0603	0.45	K	4000		
107	0003	0.8	Α	4000	_	
	0306 ※	0.50	V	_	4000	
		0.45	К	4000		
212	0805	0.85	D	4000		
212		1.25	G	10000	3000	
	0508 ※	0.85	D	4000	_	
		0.85	D	4000	_	
316	1206	1.15	F	_	3000	
310	1200	1.25	G		3000	
		1.6	L	_	2000	
		0.85	D			
		1.15	F	] _	2000	
325	1210	1.9	N	_	2000	
	Ī	2.0 max	Υ	<u>]</u>		
		2.5	М	_	1000	
432	1812	2.5	M	_	500	

Note : ※.LW Reverse type(□WK)

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### ●316TYPE

[Temperature Characteristic LD : X5R] 1.6mm thickness (L)

Part number 1	Part number 2	Rated voltage [V]	Temperat characteri		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
UMK316 LD105□L-T		50		X5R	1 μ	±10, ±20	10	150	1.6±0.20	R
GMK316BLD475[]L-T		35		X5R	4.7 μ	±10, ±20	10	150	1.6±0.30	R
TMK316BLD106□L-T		25		X5R	10 μ	±10, ±20	10	150	1.6±0.30	R

### ●325TYPE

[Temperature Characteristic LD · X5R] 1 9mm thickness (N)

	Part number 1	Part number 2	Rated voltage [V]	characteristics		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
UN	/K325 LD105∏N-T		50		X5R	1 μ	±10, ±20	10	200	1.9±0.20	R

[Temperature Characteristic LD : X5R] 2.5mm thickness (M)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
UMK325 LD155∏M-P		50		X5R	1.5 μ	±10, ±20	5	150	2.5±0.20	R
UMK325 LD475∏M-P		50		X5R	4.7 μ	±10, ±20	10	200	2.5±0.20	R

### Medium-High Voltage Multilayer Ceramic Capacitors 105TYPE

[Temperature Characteristic B7 : X7R] 0.5mm thickness(V)

Part number 1	Part number 2	Rated voltage [V]	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK105 B7221 ŪV-F			X7R	220 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7331 ŪV-F			X7R	330 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7471 ŪV-F			X7R	470 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7681 □V-F			X7R	680 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7102 U-F		100	X7R	1000 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7152□V-F			X7R	1500 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7222 ŪV-F			X7R	2200 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7332□V-F			X7R	3300 р	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7472 ŪV-F			X7R	4700 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R

[Temperature Characteristic CG : CG/C0G] 0.5mm thickness(V)

Part number 1	Part number 2	Rated voltage [V]	Tempe charact	erature eristics	Capacitance [F]	Capacitance tolerance [%]	Q (at 1MHz) min	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK105 CG080DV-F			CG	COG	8 p	±0.5pF	560	200	$0.5 \pm 0.05$	R
HMK105 CG090DV-F			CG	COG	9 p	±0.5pF	580	200	$0.5 \pm 0.05$	R
HMK105 CG100DV-F			CG	COG	10 p	±0.5pF	600	200	$0.5 \pm 0.05$	R
HMK105 CG120JV-F			CG	COG	12 p	±5%	640	200	$0.5 \pm 0.05$	R
HMK105 CG150JV-F		- -	CG	COG	15 p	±5%	700	200	$0.5 \pm 0.05$	R
HMK105 CG180JV-F			CG	COG	18 p	±5%	760	200	$0.5 \pm 0.05$	R
HMK105 CG220JV-F			CG	COG	22 p	±5%	840	200	$0.5 \pm 0.05$	R
HMK105 CG240JV-F		100	CG	COG	24 p	±5%	880	200	$0.5 \pm 0.05$	R
HMK105 CG270JV-F		100	CG	COG	27 p	±5%	940	200	$0.5 \pm 0.05$	R
HMK105 CG330JV-F			CG	COG	33 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG390JV-F			CG	COG	39 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG470JV-F			CG	COG	47 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG560JV-F			CG	COG	56 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG680JV-F			CG	COG	68 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG820JV-F			CG	COG	82 p	±5%	1000	200	0.5±0.05	R
HMK105 CG101JV-F			CG	COG	100 p	±5%	1000	200	0.5±0.05	R

### ●107TYPE

[Temperature Characteristic BJ : B/X5R] 0.8mm thickness(A)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK107 BJ102□A-T			В	X5R*1	1000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ152□A-T			В	X5R*1	1500 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ222□A-T			В	X5R*1	2200 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ332∏A-T			В	X5R*1	3300 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ472□A-T			В	X5R*1	4700 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ682□A-T			В	X5R*1	6800 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ103∏A-T		100	В	X5R*1	0.01 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ153∏A-T			В	X5R*1	0.015 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ223∏A-T			В	X5R*1	0.022 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ333∏A-T			В	X5R*1	0.033 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ473∏A-T			В	X5R*1	0.047 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ104∏A-T		]	В	X5R*1	0.1 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 BJ224□A-TE			В	X5R*1	0.22 μ	±10, ±20	3.5	150	$0.8 \pm 0.10$	R/W

 $\begin{tabular}{c} \textbf{[Temperature Characteristic C7:X7S]} & 0.8mm thickness(A) \end{tabular}$ 

Part	number 1	Part number 2	Rated voltage [V]	charact		Capacitance [F]	Capacitance tolerance [%]	tan ô [%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK107 C	224[]A-TE		100		X7S	0.22 μ	±10, ±20	3.5	150	0.8±0.10	R/W

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[Temperature Characteristic B7 : X7R ] 0.8mm thickness(A)										
Part number 1	Part number 2	Rated voltage [V]	Temper characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK107 B7102□A-T				X7R	1000 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7152□A-T				X7R	1500 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7222□A-T				X7R	2200 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7332∏A-T				X7R	3300 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7472□A-T				X7R	4700 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7682∏A-T		100		X7R	6800 p	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7103∏A-T		100		X7R	0.01 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7153∏A-T				X7R	0.015 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7223□A-T				X7R	0.022 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7333∏A-T				X7R	0.033 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7473∏A-T				X7R	0.047 μ	±10, ±20	3.5	200	$0.8 \pm 0.10$	R
HMK107 B7104[]A-T				X7R	0.1 μ	±10, ±20	3.5	200	0.8±0.10	R

[Temperature Characteristic SD : Standard ] 0.8mm thickness(A)									
Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK107 SD101KA-T				100 p	±10	0.1	200	0.8±0.10	R
HMK107 SD121KA-T		100	Standard Type	120 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD151KA-T		100		150 p	±10	0.1	200	0.8±0.10	R
HMK107 SD181KA-T				180 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD221KA-T				220 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD271KA-T				270 р	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD331KA-T				330 р	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD391KA-T				390 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD471KA-T		100	Standard Type	470 p	±10	0.1	200	0.8±0.10	R
HMK107 SD561KA-T				560 p	±10	0.1	200	0.8±0.10	R
HMK107 SD681KA-T				680 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD821KA-T		]	-	820 p	±10	0.1	200	$0.8 \pm 0.10$	R
HMK107 SD102KA-T				1000 p	±10	0.1	200	$0.8 \pm 0.10$	R

### 212TYPE

[Temperature Characteristic BJ : B/X5R] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage [V]		erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK212 BJ103∏G-T			В	X5R*1	0.01 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ153∏G-T			В	X5R*1	0.015 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ223∏G-T			В	X5R*1	0.022 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ333∏G-T			В	X5R*1	0.033 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ473∏G-T		100	В	X5R*1	0.047 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ683∏G-T		100	В	X5R*1	0.068 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ104∏G-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ224∏G-T			В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ474∏G-TE			В	X5R*1	0.47 μ	±10, ±20	3.5	150	1.25±0.10	R
HMK212BBJ105∏G-TE			В	X5R*1	1 μ	±10, ±20	3.5	150	1.25+0.20/-0	R/W
QMK212 BJ472 G-T			В	X5R*1	4700 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ682[]G-T			В	X5R*1	6800 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ103[]G-T		250	В	X5R*1	0.01 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ153[]G-T			В	X5R*1	0.015 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ223 G-T			В	X5R*1	0.022 μ	±10, ±20	2.5	150	1.25±0.10	R

[Temperature Characteristic BJ : B/X5R] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
QMK212 BJ102[]D-T			В	X5R*1	1000 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ152[]D-T		250	В	X5R*1	1500 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ222□D-T		230	В	X5R*1	2200 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 BJ332∏D-T			В	X5R*1	g 0088	±10, ±20	2.5	150	$0.85 \pm 0.10$	R

[Temperature Characteristic C7 : X7S] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage [V]	Temper characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK212 C7474[]G-TE		100		X7S	0.47 μ	±10, ±20	3.5	150	1.25±0.10	R
UMK212DC7105∏C_TE		100		Y79	1 11	+10 +20	2.5	150	1 25+0 20 /-0	D/W

[Temperature Characteristic B7 : X7R] 1.25mm thickness (G)

Temperature Characterist		Rated voltage	Temperature	Capacitance	Capacitance	tan δ	HTLT		Soldering
Part number 1	Part number 2	[V]	characteristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK212 B7103[]G-T			X7R	0.01 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7153∏G-T			X7R	0.015 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7223[]G-T			X7R	0.022 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7333∏G-T		100	X7R	0.033 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7473∏G-T		100	X7R	0.047 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7683∏G-T			X7R	0.068 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7104[]G-T			X7R	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7224 G-T			X7R	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
QMK212 B7472 G-T			X7R	4700 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7682 G-T			X7R	6800 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7103 G-T		250	X7R	0.01 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7153[]G-T			X7R	0.015 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7223 G-T			X7R	0.022 μ	±10, ±20	2.5	150	1.25±0.10	R

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[Temperature Characterist	ic B7 : X7R】 0.85mm th	ickness(D)							
		Rated voltage	Temperature	Capacitance	Capacitance	tan δ	HTLT		Soldering
Part number 1	Part number 2	[V]	characteristics	[F]	tolerance [%]		Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
QMK212 B7102[]D-T			X7R	1000 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 B7152 D-T		250	X7R	1500 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7222 D-T		230	X7R	2200 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 B7332□D-T			X7R	3300 p	±10, ±20	2.5	150	0.85±0.10	R

Temperature Characterist	ic SD : Standard 】 0.85r	nm thickness(D	))						
		Rated voltage	Temperature	Capacitance	Capacitance	tan δ	HTLT	40	Soldering
Part number 1	Part number 2	[V]	characteristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK212 SD222KD-T		100		2200 p	±10	0.1	200	$0.85 \pm 0.10$	R
HMK212 SD472KD-T		100		4700 p	±10	0.1	200	$0.85 \pm 0.10$	R
QMK212 SD101KD-T				100 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD121KD-T				120 p	±10	0.1	150	0.85±0.10	R
QMK212 SD151KD-T				150 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD181KD-T				180 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD221KD-T			Standard Type	220 p	±10	0.1	150	0.85±0.10	R
QMK212 SD331KD-T		250	Standard Type	330 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD391KD-T		230		390 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD471KD-T				470 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD561KD-T				560 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD681KD-T				680 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD821KD-T				820 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD102KD-T				1000 p	±10	0.1	150	$0.85 \pm 0.10$	R

					_	
1	Temperature	Characteristic SI	٠ ر	Standard	1	1.25mm thickness (G)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK212 SD392KG-T		100	Standard Type	3900 р	±10	0.1	200	1.25±0.10	R

### **316TYPE**

[Temperature Characteristic BJ : B/X5R] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]		erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316 BJ473∏L-T			В	X5R*1	0.047 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ683∏L-T			В	X5R*1	0.068 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ104□L-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ154□L-T			В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ224□L-T		100	В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ334□L-T			В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ474□L-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ105□L-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316ABJ225□L-TE			В	X5R*1	2.2 μ	±10, ±20	3.5	150	1.6±0.20	R/W
QMK316 BJ333□L-T			В	X5R*1	0.033 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ473□L-T		250	В	X5R*1	0.047 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ683□L-T		230	В	X5R*1	0.068 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ104 L-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 BJ153□L-T		630	В	X5R*1	0.015 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316 BJ223□L-T		030	В	X5R*1	0.022 μ	±10, ±20	2.5	120	1.6±0.20	R

### [Temperature Characteristic BJ : B/X5R] 1.15mm thickness(F)

Post sumbour 1	Rated voltage	Temperature		Capacitance	Capacitance	tan δ	HTLT		Soldering	
Part number 1	Part number 2	[V]		teristics	[F]	tolerance [%]		Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
SMK316 BJ102[F-T			В	X5R*1	1000 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ152[F-T			В	X5R*1	1500 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ222 F-T			В	X5R*1	2200 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ332□F-T		630	В	X5R*1	3300 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ472□F-T			В	X5R*1	4700 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ682∏F-T			В	X5R*1	6800 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ103[F-T			В	X5R*1	0.01 μ	±10, ±20	2.5	120	1.15±0.10	R

[Temperature Characteristic C7 : X7S] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	Temperat characteris		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316AC7225□L-TE		100		X7S	2.2 μ	±10, ±20	3.5	150	1.6±0.20	R/W

[Temperature Characteristic B7 : X7R] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316 B7473□L-T			X7R	0.047 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7683□L-T			X7R	0.068 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7104□L-T			X7R	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7154□L-T		100	X7R	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7224□L-T		100	X7R	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7334□L-T			X7R	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7474□L-T			X7R	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7105□L-T			X7R	1 μ	±10, ±20	3.5	200	1.6±0.20	R
QMK316 B7333[L-T			X7R	0.033 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7473[L-T		250	X7R	0.047 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7683[L-T		230	X7R	0.068 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7104[L-T			X7R	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 B7153[]L-T		630	X7R	0.015 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316 B7223□L-T		030	X7R	0.022 μ	±10, ±20	2.5	120	1.6±0.20	R

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Temperature Characterist	ic B7 : X7R】 1.15mm th	nickness(F)								0 11 1
Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance	Capacitance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
		[V]	cnarac		[F]	tolerance [%]	[%]	Rated voltage x %		W:Wave
SMK316 B7102 F-T		4		X7R	1000 p	±10, ±20 ±10, ±20	2.5	120	1.15±0.10	R R
SMK316 B7152[F-T		-		X7R X7R	1500 p		2.5 2.5	120 120	1.15±0.10	R
SMK316 B7222∏F-T SMK316 B7332∏F-T		630		X7R X7R	2200 p	±10, ±20 ±10, ±20	2.5	120	1.15±0.10 1.15±0.10	
SMK316 B7472[]F-T		030		X7R X7R	3300 p 4700 p	±10, ±20 ±10, ±20	2.5	120	1.15±0.10 1.15±0.10	R R
SMK316 B7682[F-T		-		X7R X7R	6800 p	±10, ±20 ±10, ±20	2.5	120	1.15±0.10 1.15±0.10	R
SMK316 B7103[F-T		+		X7R X7R	0.01 μ	±10, ±20 ±10, ±20	2.5	120	1.15±0.10 1.15±0.10	R
SMK310 B/103UF-1				Λ/Κ	0.01 μ	±10, ±20	2.3	120	1.15±0.10	K
Temperature Characterist	ic SD : Standard 】 1.6n	nm thickness(L)								0 11 1
Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316 SD223KL-T		100	Standa	ard Type	0.022 μ	±10	0.1	200	1.6±0.20	R
QMK316 SD103KL-T		250			0.01 μ	±10	0.1	150	1.6±0.20	R
325TYPE										
Temperature Characterist	ic BJ : B/X5R】 2.5mm	thickness(M)						HTLT		Soldering
Part number 1	Part number 2	Rated voltage		erature	Capacitance	Capacitance	tan δ		Thickness*3 [mm]	R:Reflow
T di Citambol T	T di Citambol E	[V]	charac	teristics	[F]	tolerance [%]	[%]	Rated voltage x %	THICKNESS [IIIII]	W:Wave
HMK325 BJ225∏M-P		100	В	X5R*1	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK325 BJ475∏M−PE		100	В	X5R*1	4.7 μ	±10, ±20	3.5	150	2.5±0.20	R
'T	:- D   - D /VED 1 10	Al-1-I(NI)								
Temperature Characterist	I.9mm							HTLT		Solderin
Part number 1	Part number 2	Rated voltage		erature	Capacitance [F]	Capacitance tolerance [%]	tan δ		Thickness*3 [mm]	R:Reflow
		[V]		teristics			[%]	Rated voltage x %		W:Wave
HMK325 BJ154∏N-T			В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ224∏N-T		<b>.</b>	В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ334∏N-T			В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ474□N-T		100	В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ684□N-T			В	X5R*1	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ105∏N-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ475∏N-TE			В	X5R*1	4.7 μ	±10, ±20	3.5	150	1.9±0.20	R
MK325 BJ473[N-T		4	В	X5R*1	0.047 μ	±10, ±20	2.5	150	1.9±0.20	R
MK325 BJ104[N-T		250	В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ154[N-T		4	В	X5R*1	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ224[]N-T			В	X5R*1	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 BJ223 N-T			В	X5R*1	0.022 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ333∏N-T		630	В	X5R*1	0.033 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ473∏N-T			В	X5R*1	0.047 μ	±10, ±20	2.5	120	1.9±0.20	R
Temperature Characterist	ic B.I · B/X5R 1 115mm	thickness (F)								
Temperature onaracterist	IO BO . B/ XOTY T.TOTHIN		-		0 "	2 :		HTLT		Soldering
Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow
										W:Wave
HMK325 BJ104∏F-T		100	В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.15±0.10	R
Temperature Characterist	ic B7 : X7R】 2.5mm thi	ckness(M)								
•		Rated voltage	Tomp	erature	Capacitance	Capacitance	$ an\delta$	HTLT		Soldering
Part number 1	Part number 2	[V]		teristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow
INVESTIGATION D			onar ao					_	0.5   0.00	W:Wave
HMK325 B7225∏M-P		100		X7R	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
Temperature Characterist	ic B7 : X7R】 1.9mm thi	ckness(N)								
		Rated voltage	Tem-	erature	Capacitance	Capacitance	$ an\delta$	HTLT		Solderin
Part number 1	Part number 2	[V]		erature teristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow
WHITE PRINCES			2.10.00					_	40	W:Wave
HMK325 B7154□N-T		-		X7R	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7224□N-T		-		X7R	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7334□N-T		100		X7R	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7474□N-T		-		X7R	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7684□N-T		4		X7R	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
MK325 B7105∏N-T		+		X7R	1 μ	±10, ±20	3.5	200	1.9±0.20	R
QMK325 B7473∏N-T		4		X7R	0.047 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7104∏N-T		250		X7R	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7154□N-T		200		X7R	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7224[N-T		1		X7R	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 B7223∏N-T				X7R	0.022 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7333∏N-T		630		X7R	0.033 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7473∏N-T				X7R	0.047 μ	±10, ±20	2.5	120	1.9±0.20	R
T	: 07 . V70 1 05									
Temperature Characterist	ic U / : X /S 】 2.5mm thi							HTLT		Solderin
Part number 1	Part number 2	Rated voltage		erature	Capacitance	Capacitance	tan δ		Thickness*3 [mm]	R:Reflov
		[V]	charac	teristics	[F]	tolerance [%]	[%]	Rated voltage x %		W:Wave
HMK325 C7475∏M-PE		100		X7S	4.7 μ	±10, ±20	3.5	150	2.5±0.20	R
Tomporatura Characterist	in C7 · Y7C1 10 41-	oknoso (NI)								
Temperature Characterist	ICOI. AIOJ I.SMM thi							HTLT		Solderin
	Part number 2	Rated voltage		erature	Capacitance	Capacitance	tan δ		Thickness*3 [mm]	R:Reflov
Part number 1	. a. c . alliboi L	[V]	charac	teristics	[F]	tolerance [%]	[%]	Rated voltage x %	[IIIII] Scottyouri	W:Wave
Part number 1		2.3								
		100		X7S	4.7 μ	±10, ±20	3.5	150	1.9±0.20	R
HMK325 C7475∏N-TE		100		X7S	4.7 μ	±10, ±20	3.5	150	1.9±0.20	
HMK325 C7475∏N-TE		100		X7S	4.7 μ	±10, ±20	3.5		1.9±0.20	R
HMK325 C7475∏N-TE Temperature Characterist	ic B7 : X7R】 1.15mm th	100	Temp	X7S erature	Capacitance	Capacitance	3.5 tan δ	150		R
Part number 1  HMK325 C7475[]N-TE  [Temperature Characterist  Part number 1		100 nickness(F)							1.9±0.20 Thickness*3 [mm]	

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### ●432TYPE

[Temperature Characteristic BJ : B/X5R] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK432 BJ474∏M-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ105∏M-T		100	В	X5R*1	1 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ155∏M-T		100	В	X5R*1	1.5 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK432 BJ225∏M−T			В	X5R*1	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
QMK432 BJ104[M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ224[M-T		250	В	X5R*1	0.22 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ334[M-T		230	В	X5R*1	0.33 μ	±10, ±20	2.5	150	2.5±0.20	R
QMK432 BJ474[M-T			В	X5R*1	0.47 μ	±10, ±20	2.5	150	$2.5 \pm 0.20$	R
SMK432 BJ473 M-T			В	X5R*1	0.047 μ	±10, ±20	2.5	120	2.5±0.20	R
SMK432 BJ683[M-T		630	В	X5R*1	0.068 μ	±10, ±20	2.5	120	2.5±0.20	R
SMK432 BJ104 M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	120	2.5±0.20	R

[Temperature Characteristic B7 : X7R] 2.5mm thickness(M)

	Part number 1	Part number 2	Rated voltage [V]	Tempera characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HI	MK432 B7474∏M-T				X7R	0.47 μ	±10, ±20	3.5	200	2.5±0.20	R
HI	MK432 B7105∏M−T		100		X7R	1 μ	±10, ±20	3.5	200	2.5±0.20	R
HI	MK432 B7155∏M-T		100		X7R	1.5 μ	±10, ±20	3.5	200	2.5±0.20	R
HI	MK432 B7225∏M-T				X7R	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
Q	MK432 B7104∏M-T				X7R	0.1 μ	±10, ±20	2.5	150	2.5±0.20	R
Q	MK432 B7224[M-T		250		X7R	0.22 μ	±10, ±20	2.5	150	2.5±0.20	R
Q	MK432 B7334∏M-T		230		X7R	0.33 μ	±10, ±20	2.5	150	2.5±0.20	R
Q	MK432 B7474[M-T				X7R	0.47 μ	±10, ±20	2.5	150	2.5±0.20	R
SI	/K432 B7473∏M−T				X7R	0.047 μ	±10, ±20	2.5	120	2.5±0.20	R
SI	/K432 B7683∏M−T		630		X7R	0.068 μ	±10, ±20	2.5	120	2.5±0.20	R
SI	/K432 B7104∏M-T				X7R	0.1 μ	±10, ±20	2.5	120	2.5±0.20	R

### <u>LW Reversal Decoupling Capacitors(LWDC<sup>™</sup>)</u> ■105TYPE

[Temperature Characteristic BJ : X5R] 0.3mm thickness(P)

Part number 1	Part number 2	Rated voltage [V]	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
TWK105 BJ104MP-F		25	X5R	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
EWK105 BJ224MP-F		16	X5R	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
LWK105 BJ474MP-F		10	X5R	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ104MP-F			X5R*1	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
JWK105 BJ474MP-F		6.3	X5R*1	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ105MP-F		0.3	X5R	1 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 BJ225MP-F	, and the second		X5R	2.2 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 BJ224MP-F		4	X5R	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R

[Temperature Characteristic C6 : X6S , C7 : X7S] 0.3mm thickness(P)

Part number 1	Part number 2	Rated voltage [V]	Tempe charact	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
EWK105 C6104MP-F		16		X6S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
LWK105 C7104MP-F		10		X7S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
LWK105 C6224MP-F		10		X6S	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 C7104MP-F				X7S	0.1 μ	±20	5	150	$0.3 \pm 0.05$	R
JWK105 C7224MP-F		6.3		X7S	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
JWK105 C6474MP-F				X6S	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 C6224MP-F				X6S	0.22 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 C6474MP-F		1 4		X6S	0.47 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 C6105MP-F		]		X6S	1 μ	±20	10	150	$0.3 \pm 0.05$	R
AWK105 C6225MP-F			,	X6S	2.2 μ	±20	10	150	$0.3 \pm 0.05$	R

●107TYPE 【Temperature Characteristic BJ: X5R】0.5mm thickness(V)

	_	Rated voltage	Tempe	Temperature Capacitance		e Capacitance	$tan \delta$	HTLT		Soldering
Part number 1	Part number 2	[V]	characteristics		[F]	tolerance [%]		Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
TWK107 BJ104MV-T		25		X5R*1	0.1 μ	±20	5	150	$0.5 \pm 0.05$	R
EWK107 BJ224MV-T		16		X5R*1	0.22 μ	±20	5	150	$0.5 \pm 0.05$	R
EWK107 BJ474MV-T		10		X5R*1	0.47 μ	±20	5	150	$0.5 \pm 0.05$	R
LWK107 BJ105MV-T		10		X5R	1 μ	±20	10	150	$0.5 \pm 0.05$	R
LWK107 BJ225MV-T		10		X5R	2.2 μ	±20	10	150	$0.5 \pm 0.05$	R
JWK107 BJ105MV-T				X5R*1	1 μ	±20	10	150	$0.5 \pm 0.05$	R
JWK107 BJ225MV-T		6.3		X5R	2.2 μ	±20	10	150	$0.5 \pm 0.05$	R
JWK107 BJ475MV-T				X5R	4.7 μ	±20	10	150	$0.5 \pm 0.05$	R
AWK107 BJ106MV-T		4		X5R	10 μ	±20	10	150	$0.5 \pm 0.05$	R

Part number 1	Part number 2	Rated voltage [V]	Temper characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
TWK107 B7104MV-T		25		X7R	0.1 μ	±20	5	150	0.5±0.05	R
EWK107 B7224MV-T		16		X7R	0.22 μ	±20	5	150	$0.5 \pm 0.05$	R
EWK107 B7474MV-T		10		X7R	0.47 μ	±20	5	150	0.5±0.05	R
JWK107 C7105MV-T		6.3		X7S	1 μ	±20	10	150	$0.5 \pm 0.05$	R
AWK107 C7225MV-T		4		X7S	2.2 μ	±20	10	150	$0.5 \pm 0.05$	R
AWK107 C6475MV-T		4		X6S	4.7 μ	±20	10	150	0.5±0.05	R
PWK107 C6106MV-T		2.5		X6S	10 μ	±20	10	150	0.5±0.05	R

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### **Multilayer Ceramic Capacitors**

### ■PACKAGING

### 1)Minimum Quantity

Τ (ΓΙΔ)	Thick	ness	Standard q	uantity [pcs]	
Type(EIA)	mm	code	Paper tape	Embossed tape	
□MK021(008004)	0.125	К	_	50000	
☐MK042(01005)	0.2	C, D		40000	
□VS042(01005)	0.2	С	<b>–</b>	40000	
□MK063(0201)	0.3	P, T	15000		
□WK105(0204) ※	0.3	Р	10000	] _	
	0.13	Н	_	20000	
	0.18	E	_	15000	
☐MK105(0402)	0.2	С	20000		
	0.3	Р	15000		
	0.5	V	10000	_	
□VK105(0402) ※	0.5	W	10000		
□MK107(0603)	0.45	K	4000		
□WK107(0306) ※	0.5	V	_	4000	
☐MR107(0603)	0.8	Α			
□MK212(0805)	0.45	К	4000	_	
□WK212(0508) ※	0.85	D			
□MR212(0805)	125	G	_	3000	
	0.85	D	4000	_	
□MK316(1206)	1.15	F		0000	
□MR316(1206)	125	G	_	3000	
	1.6	L	_	2000	
	0.85	D			
	1.15	F		0000	
□MK325(1210)	1.9	N	_	2000	
□MR325(1210)	2 Omay	<b>Y</b>			

М

Note: \* LW Reverse type.

Chip

□MK432(1812)

2.0max. 2.5

# © Top tape Card board carrier tape Base tape Sprocket hole Chip cavity Chip filled Chip filled

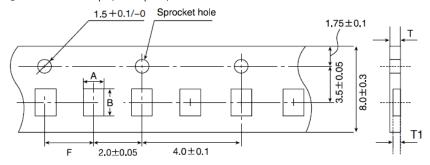
1000

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### 3 Representative taping dimensions

### Paper Tape (8mm wide)

### ● Pressed carrier tape (2mm pitch)

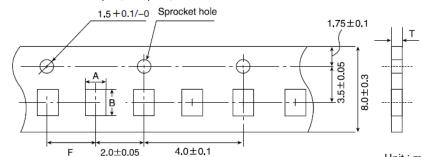


		Onit : mm	Onit · mm				
Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Ti	nickness		
Type(EIA)	Α	В	F	Т	T1		
☐MK063(0201)	0.37	0.67		0.45max.	0.42max.		
□WK105(0204) ※			2.0±0.05	0.45max.	0.42max.		
☐MK105(0402) (*1 C)	0.65	1.15	2.0±0.03	0.4max.	0.3max.		
□MK105(0402) (*1 P)				0.45max.	0.42max.		

Note \*1 Thickness, C:0.2mm ,P:0.3mm. \* LW Reverse type.

Unit:mm

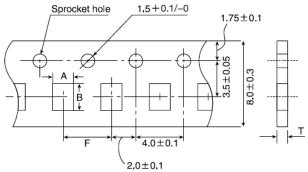
### ●Punched carrier tape (2mm pitch)



			Unit - mm	
Type(EIA)	Chip Cavity		Insertion Pitch	Tape Thickness
Type(EIA)	Α	В	F	Т
□MK105 (0402) □VK105 (0402)	0.65	1.15	2.0±0.05	0.8max.

Unit:mm

### ●Punched carrier tape (4mm pitch)



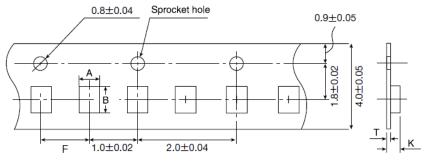
	2.0±0.1	Unit	: mm	
Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Thickness
Type(EIA)	Α	В	F	Т
☐MK107(0603)				
□WK107(0306) ※	1.0	1.8		1.1max.
□MR107(0603)			4.0±0.1	
☐MK212(0805)	1.65	2.4	4.0 ± 0.1	
□WK212(0508) ※	1.00	2.4		1.1max.
□MK316(1206)	2.0	3.6		

Note: Taping size might be different depending on the size of the product. 💥 LW Reverse type.

Unit:mm

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### Embossed tape (4mm wide)

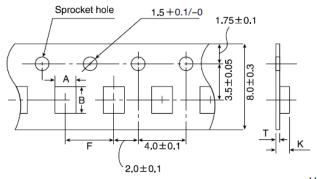


Unit: mm

Tura/EIA)	Chip Cavity		Insertion Pitch	Tape Th	nickness
Type(EIA)	Α	В	F	K	Т
☐MK021(008004)	0.135	0.27			
☐MK042(01005)	0.00	0.40	1.0±0.02	0.5max.	0.25max.
□VS042(01005)	0.23	0.43			

Unit:mm

### Embossed tape (8mm wide)



Unit: mm

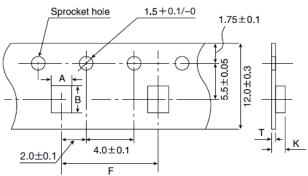
Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Ti	nickness
Type(EIA)	Α	В	F	K	Т
☐MK105(0402)	0.6	1.1	2.0±0.1	0.6max	0.2±0.1
□WK107(0306) ※	1.0	1.8		1.3max.	0.25±0.1
□MK212(0805) □MR212(0805)	1.65	2.4			
□MK316(1206) □MR316(1206)	2.0	3.6	4.0±0.1	3.4max.	0.6max.
□MK325(1210) □MR325(1210)	2.8	3.6			

Note: 

LW Reverse type.

Unit:mm

### Embossed tape (12mm wide)



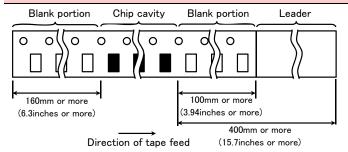
Unit: mm

Type(EIA)	Chip Cavity		Insertion Pitch	Tape Ti	nickness
Type(EIA)	Α	В	F	K	Т
□MK432(1812)	3.7	4.9	8.0±0.1	4.0max.	0.6max.

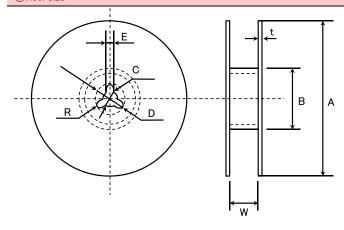
Unit:mm

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### 4 Trailer and Leader



### **5**Reel size



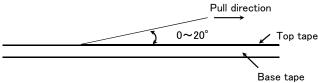
Α	В	С	D	E	R
$\phi$ 178 ± 2.0	<i>ф</i> 50min.	$\phi$ 13.0 $\pm$ 0.2	$\phi$ 21.0 ± 0.8	2.0±0.5	1.0

	Т	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

Unit:mm

### $\textbf{\^{6}} \textbf{Top Tape Strength}$

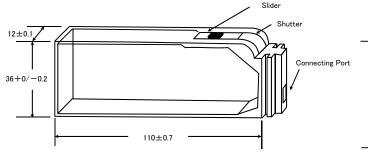
The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.

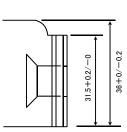


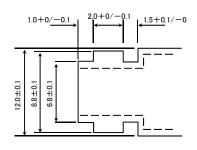
### **7**Bulk Cassette

The exchange of individual specification is necessary.

Please contact Taiyo Yuden sales channels.







Unit:mm

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### Medium-High Voltage Multilayer Ceramic Capacitor

### ■RELIABILITY DATA

1. Operating Tempe	rature Range	
	Temperature Compensating(Class1)	CG : -55 to +125°C
Specified Value	High Permittivity(Class2)	X7R, X7S : -55 to +125°C X5R : -55 to +85°C B : -25 to +85°C SD : -55 to +125°C
2. Storage Tempera	ture Range	
	Temperature Compensating(Class1)	CG : −55 to +125°C
Specified Value	High Permittivity(Class2)	X7R, X7S : -55 to +125°C X5R : -55 to +85°C B : -25 to +85°C SD : -55 to +125°C
3. Rated Voltage		
	Temperature Compensating(Class1)	100VDC(HMK)
Specified Value	High Permittivity (Class2)	100VDC(HMK), 250VDC(QMK), 630VDC(SMK)
4. Withstanding Volt	age (Between terminals)	
Specified Value	No breakdown or damage	
Test Methods and Remarks	Applied voltage  Duration  Charge/discharge current  : Rated voltage > 1 to 5sec. : 50mA max.	×2.5(HMK), Rated voltage ×2(QMK), Rated voltage × 1.2(SMK)
5 1 1 1 1 D 1 1		
5. Insulation Resista		10000 NO :
Specified Value	Temperature Compensating(Class1)  High Permittivity (Class2)	10000 MΩ min. 100MΩ• μF or 10GΩ whichever is smaller.
Test Methods and Remarks		HMK, QMK), 500V(SMK)
6. Capacitance (To	lerance)	
Specified Value	Temperature Compensating(Class1)	0.2pF≦C≦5pF : ±0.25pF 0.2pF≦C≤10pF : ±0.5pF C>10pF : ±5% or ±10%
	High Permittivity (Class2)	±10%, ±20%
Test Methods and	Temperature Compensating(Class1)	Measuring frequency       : 1MHz±10%         Measuring voltage       : 0.5∼5Vrms         Bias application       : None
Remarks	High Permittivity(Class2)	Measuring frequency       : 1kHz±10%         Measuring voltage       : 1±0.2Vrms         Bias application       : None
7. Q or Dissipation I	Factor	
Specified Value	Temperature Compensating(Class1)	$C < 30pF : Q \ge 400 + 20C$ $C \ge 30pF : Q \ge 1000$ (C:Nominal capacitance)
	High Permittivity (Class2)	3.5%max(HMK),2.5%max(QMK, SMK)
Test Methods and	Temperature Compensating(Class1)	Measuring frequency       : 1MHz±10%         Measuring voltage       : 0.5∼5Vrms         Bias application       : None
Remarks	High Permittivity (Class2)	Measuring frequency       : 1kHz±10%         Measuring voltage       : 1±0.2Vrms         Bias application       : None

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### 8. Temperature Characteristic of Capacitance Temperature Compensating(Class1) CG $:0\pm30$ ppm/°C(-55 to +125°C) В : $\pm 10\%(-25 \text{ to } +85^{\circ}\text{C})$ : ±15%(-55 to +85°C) : ±15%(-55 to +125°C X5R Specified Value High Permittivity (Class2) X7R : $\pm 22\%(-55 \text{ to } +125^{\circ}\text{C})$ X7S SD : $-(-55 \text{ to } +125^{\circ}\text{C})$ Capacitance value at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the CG, B, X5R, X7R, X7S, SD Step Minimum operating temperature

Test Methods and Remarks

2 20°C 25°C 3 Maximum operating temperature

 $(C-C_2)$ × 100(%)  $C_2$ 

C : Capacitance value in Step 1 or Step 3

C2: Capacitance value in Step 2

9. Deflection		
Specified Value	Temperature Compensating(Class1)	Appearance : No abnormality Capacitance change : Within $\pm 5\%$ or $\pm 0.5$ pF, whichever is larger.
Specified Value	High Permittivity (Class2)	Appearance : No abnormality Capacitance change : Within±10%
Test Methods and Remarks	Warp : 1mm Duration : 10sec. Test board : Glass epoxy-resin substrate Thickness : 1.6mm  Canacitance measurement shall be conducted a	Board R-230 Warp    45±2   45±2
	Capacitance measurement shall be conducted v	with the board bent.

10. Adhesive Streng	gth of Terminal Electrodes	
Specified Value	Temperature Compensating(Class1)	No terminal separation or its indication.
Specified value	High Permittivity (Class2)	No terminal separation or its indication.
Test Methods and Remarks	Applied force : 5N  Duration : 30±5sec.	Hooked jig  R=05  Chip  Chip

11. Solderability					
Specified Value Temperature Compensating(		Class1)	ass1)		now colder
Specified value	High Permittivity (Class2)		At least 95% of terminal electrode is covered by new solder		new solder
		Eutecti	c solder	Lead-free solder	
Test Methods and	Solder type	H60A d	or H63A	Sn-3.0Ag-0.5Cu	
Remarks	Solder temperature	230	±5℃	245±3°C	
	Duration		4±1	sec.	

		Appearance	: No abnormality
		Capacitance change	: Within $\pm 2.5\%$ or $\pm 0.25$ pF, whichever is larger.(HMK)
	Temperature Compensating(Class1)	Q	: Initial value
		Insulation resistance	: Initial value
0 'C 17/1		Withstanding voltage	(between terminals): No abnormality
Specified Value		Appearance	: No abnormality
		Capacitance change	: Within $\pm 15\%$ (HMK), $\pm 10\%$ (QMK, SMK)
	High Permittivity (Class2)	Dissipation facto	: Initial value
		Insulation resistance	: Initial value
		Withstanding voltage	(between terminals): No abnormality

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	Temperature Compensating(Class1)
Preconditioning	None
Solder temperature	270±5°C
Duration	3±0.5sec.
Duck sating a suditions	80 to 100°C, 2 to 5 min.
Preheating conditions	150 to 200°C, 2 to 5min.
Recovery	24±2hrs under the standard condition Note3

### Test Methods and Remarks

	High Permittivity (Class2)
Preconditioning	Thermal treatment (at 150°C for 1hr) Note1
Solder temperature	270±5°C
Duration	3±0.5sec.
Dual-action conditions	80 to 100°C, 2 to 5 min.
Preheating conditions	150 to 200°C, 2 to 5min.
Recovery	24±2hrs under the standard condition Note3

13. Temperature Cy	cle (Thermal Shock)					
Specified Value	Temperature Compens	ating(Class1)	Appearance Capacitance change Q Insulation resistance Withstanding voltage	: No abnormality : Within ±2.5% or : : Initial value : Initial value (between terminal	·	-
	High Permittivity(Clas	ss2)	Appearance Capacitance change Dissipation facto Insulation resistance Withstanding voltage	: No abnormality : Within±15%(HMH : Initial value : Initial value (between terminal		
		С	lass 1		Class 2	
	Preconditioning		None	Thermal tr	eatment (at 150°C Note 1	ofor 1 hr)
		Step	Temperatur	re(°C)	Time (min.)	
Test Methods and		1	Minimum operating		30±3	
Remarks	1 cycle	2	Normal temp	erature	2 to 3	
		3	Maximum operating temperatur		30±3	
		4 Normal temper		perature 2 to 3		
	Number of cycles		5 :	times		
	Recovery	6 to 24 hrs (Stand	dard condition)Note 3	24±2 hrs	(Standard condition	on) Note 3

14. Humidity (Stead	dy state)			
Specified Value	Temperature Compensating(Class1)		Appearance Capacitance chang Q Insulation resistance	: $C < 10pF : Q \ge 200 + 10C$ $10 \le C < 30pF : Q \ge 275 + 2.5C$ $C \ge 30pF : Q \ge 350 (C : Nominal capacitance)$
	High Permittivity(Class2)		Appearance Capacitance chang Dissipation factor Insulation resistance	: 7%max(HMK), 5%max(QMK, SMK).
		Class 1		Class 2
Test Methods and Remarks	Preconditioning	None		Thermal treatment (at 150°C for 1 hr) Note 1
	Temperature	40±2°C		40±2°C
	Humidity	90 to 95%	6RH	90 to 95%RH
	Duration	500+24/-	-0 hrs	500+24/-0 hrs
	Recovery	6 to 24 hrs (Standard	condition) Note 3	24±2 hrs (Standard condition) Note 3

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15. Humidity Loadin	g			
Specified Value	Temperature Compensating(Class1)		Appearance Capacitance change Q Insulation resistance	: No abnormality : Within $\pm 7.5\%$ or $\pm 0.75$ pF, whichever is larger (HMK). : C $< 30$ pF: Q $\ge 100 + 10$ C/3 C $\ge 30$ pF: Q $\ge 200$ (C: Nominal capacitance) : 500 M $\Omega$ min.
	High Permittivity(Class2)		Appearance Capacitance change Dissipation factor Insulation resistance	: No abnormality : Within $\pm$ 15% : 7%max(HMK), 5%max(QMK, SMK). : 10M $\Omega\mu$ F or 500M $\Omega$ whichever is smaller.
Test Methods and Remarks	According to JIS 5101-1.			
		C	lass 1	Class 2
	Preconditioning	None		Voltage treatment (Rated voltage are applied for 1 hour at 40°C) Note 2
	Temperature	40±2°C		40±2°C
	Humidity	90 to 95%RH		90 to 95%RH
	Duration	500+	500+24/-0  hrs $500+24/-0  hrs$	
	Applied voltage	Rate	d voltage	Rated voltage
	Charge/discharge current	50r	nA max.	50mA max.
	Recovery	6 to 24 hrs (Stand	dard condition)Note 3	24±2 hrs (Standard condition) Note 3

16. High Temperature Loading					
Specified Value	Temperature Comper	nsating(Class1)	Appearance Capacitance cha Q Insulation resist	: C<30pF:Q≧100+10C/3 C≧30pF:Q≧200 (C:Nominal capacitance)	
	High Permittivity(Class2)		Appearance Capacitance cha Dissipation factor Insulation resist	etor : 7%max(HMK), 5%max(QMK, SMK).	
	According to JIS 510	1-1.			
		Class 1		Class 2	
	Preconditioning	None		Voltage treatment Note 2	
Test Methods and Remarks	Temperature	Maximum operating temperature		Maximum operating temperature	
	Duration	1000+48/-0 hrs		1000 + 48 / -0 hrs	
	Applied voltage	Rated voltage × 2(HMK)		Rated voltage × 2(HMK), Rated voltage × 1.5 (QMK), Rated voltage × 1.2 (SMK)	
	Charge/discharge current	50mA ma	ıx.	50mA max.	
	Recovery	6 to 24hr (Standard co	ondition) Note 3	24±2 hrs (Standard condition) Note 3	

Note1 Thermal treatment : Initial value shall be measured after test sample is heat-treated at  $150+0/-10^{\circ}$ C for an hour and kept at room temperature for  $24\pm2$ hours.

Note2 Voltage treatment: Initial value shall be measured after test sample is voltage—treated for an hour at both the temperature and voltage specified in the test conditions, and kept at room temperature for 24±2hours.

Note3 Standard condition : Temperature: 5 to  $35^{\circ}$ C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted under the following condition.

Temperature:  $20\pm2^{\circ}$ C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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### Precautions on the use of Multilayer Ceramic Capacitors

### **■**PRECAUTIONS

### 1. Circuit Design

- ◆Verification of operating environment, electrical rating and performance
- 1. A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications.

Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications.

### Precautions

- ◆Operating Voltage (Verification of Rated voltage)
  - 1. The operating voltage for capacitors must always be their rated voltage or less.
    - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less.
    - For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.
  - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

### 2. PCB Design

Precautions

- ◆Pattern configurations (Design of Land-patterns)
- 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
  - (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.
  - (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.
- ◆Pattern configurations (Capacitor layout on PCBs)

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

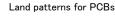
◆Pattern configurations (Design of Land-patterns)

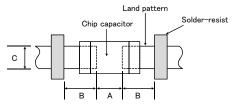
The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

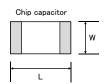
- (1) Recommended land dimensions for typical chip capacitors
- Multilayer Ceramic Capacitors : Recommended land dimensions (unit: mm)

Wave-soldering

	11410 0014011118					
Ту	ре	107	212	316	325	
Size	┙	1.6	2.0	3.2	3.2	
Size	W	0.8	1.25	1.6	2.5	
-	4	0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5	
Е	3	0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7	
(	)	0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5	







### Technical considerations

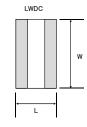
### Reflow-soldering

1101	10 11 30	Jidoi ilig							
Ту	ре	042	063	105	107	212	316	325	432
Size	L	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
Size	W	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
-	4	0.15 to 0.25	0.20 to 0.30	0.45 to 0.55	0.8 to 1.0	0.8 to 1.2	1.8 to 2.5	1.8 to 2.5	2.5 to 3.5
E	3	0.15 to 0.20	0.20 to 0.30	0.40 to 0.50	0.6 to 0.8	0.8 to 1.2	1.0 to 1.5	1.0 to 1.5	1.5 to 1.8
(	)	0.15 to 0.30	0.25 to 0.40	0.45 to 0.55	0.6 to 0.8	0.9 to 1.6	1.2 to 2.0	1.8 to 3.2	2.3 to 3.5

Note: Recommended land size might be different according to the allowance of the size of the product.

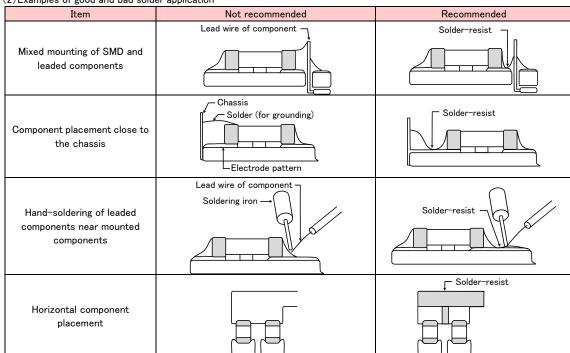
### ●LWDC: Recommended land dimensions for reflow-soldering (unit: mm)

Type		105	107	212
Size	L	0.52	0.8	1.25
Size	W	1.0	1.6	2.0
Α		0.18 to 0.22	0.25 to 0.3	0.5 to 0.7
Е	3	0.2 to 0.25	0.3 to 0.4	0.4 to 0.5
С		0.9 to 1.1	1.5 to 1.7	1.9 to 2.1



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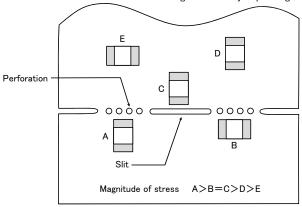
(2) Examples of good and bad solder application



- ◆Pattern configurations (Capacitor layout on PCBs)
  - 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.

Items	Not recommended	Recommended
Deflection of board		Place the product at a right angle to the direction of the anticipated mechanical stress.

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1–3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

### 3. Mounting

- ◆Adjustment of mounting machine
  - 1. When capacitors are mounted on PCB, excessive impact load shall not be imposed on them.
  - 2. Maintenance and inspection of mounting machines shall be conducted periodically.

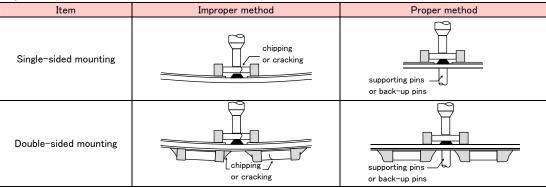
### Precautions Selection of Adhesives

1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.

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### ◆Adjustment of mounting machine

- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable.
  - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.
  - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
  - (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



### Technical considerations

2. As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors.

To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.

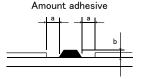
### ◆Selection of Adhesives

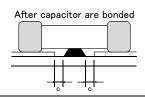
Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
  - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
  - b. The adhesive shall have sufficient strength at high temperatures.
  - c. The adhesive shall have good coating and thickness consistency.
  - d. The adhesive shall be used during its prescribed shelf life.
  - e. The adhesive shall harden rapidly.
  - f. The adhesive shall have corrosion resistance.
  - g. The adhesive shall have excellent insulation characteristics.
  - h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- (2) The recommended amount of adhesives is as follows;

[Recommended condition]

a 0.3mm min b 100 to 120 μ m	Figure	212/316 case sizes as examples
	а	0.3mm min
Adhasiyas shall not contact land	b	100 to 120 $\mu$ m
C Adriesives shall flot contact land	С	Adhesives shall not contact land





### 4. Soldering

Precautions

Technical

considerations

### ◆Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt%( in CI equivalent) of halogenated content. Flux having a strong acidity content shall not be applied.
- (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

### **♦**Soldering

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

Sn-Zn solder paste can adversely affect MLCC reliability.

Please contact us prior to usage of Sn-Zn solder.

### ◆Selection of Flux

- 1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.
- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used.

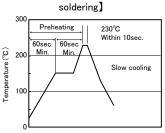
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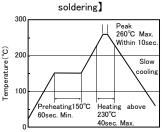
### **♦**Soldering

- · Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- · Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating : Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 130°C
- Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.
   [Reflow soldering]

[Recommended conditions for eutectic

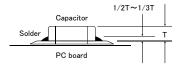


### [Recommended condition for Pb-free



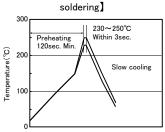
### Caution

- $\bigcirc$  The ideal condition is to have solder mass(fillet) controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as close to recommended times as possible.

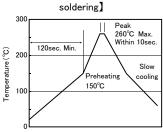


### [Wave soldering]

[Recommended conditions for eutectic



### [Recommended condition for Pb-free

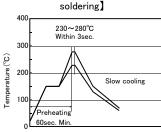


### Caution

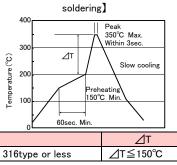
①Wave soldering must not be applied to capacitors designated as for reflow soldering only.

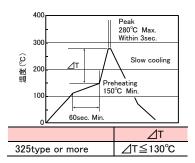
### [Hand soldering]

[Recommended conditions for eutectic



### [Recommended condition for Pb-free





### Caution

- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- ②The soldering iron shall not directly touch capacitors.

### 5. Cleaning

### ◆Cleaning conditions

### Precautions

- 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use of the cleaning. (e.g. to remove soldering flux or other materials from the production process.)
- 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics.

### Technical considerations

- 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating resulting in a degradation of the capacitor's electrical properties (especially insulation resistance).
- 2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors. In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the cracking of capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall be carefully checked;

Ultrasonic output: 20 W/l or less
Ultrasonic frequency: 40 kHz or less
Ultrasonic washing period: 5 min. or less

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### 6. Resin coating and mold 1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the capacitor's performance. 2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or destruction of capacitors. The use of such resins, molding materials etc. is not recommended.

## 7. Handling Splitting of PCB 1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board. 2. Board separation shall not be done manually, but by using the appropriate devices. Mechanical considerations Be careful not to subject capacitors to excessive mechanical shocks. (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used. (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components.

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8. Storage condi	tions
Precautions	◆Storage  1. To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.  *Recommended conditions  Ambient temperature: Below 30°C  Humidity: Below 70% RH  The ambient temperature must be kept below 40°C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.  *Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.  2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits. Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for 1hour.
Technical considerations	If capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.
<b> ※</b> RCR−2335B(S	Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA.

\*\*RCR-2335B(Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA. Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

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