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**TPS61193** 

SNVSAM8-AUGUST 2016

# **TPS61193 High-Performance Three-Channel LED Driver**

Technical

Documents

#### Features 1

- Input Voltage Operating Range 4.5 V to 40 V
- Three High-Precision Current Sinks
  - Current Matching 1% (Typical)
  - LED String Current up to 100 mA per Channel
  - Outputs can be Combined Externally for **Higher Current Capability**
- High Dimming Ratio of 10 000:1 at 100 Hz
- Integrated Boost/SEPIC for LED String Power
  - Output Voltage up to 45 V
  - Switching Frequency 300 kHz to 2.2 MHz
  - Switching Synchronization Input
  - Spread Spectrum for Lower EMI
- **Extensive Fault Detection Features** 
  - Fault Output
  - Input Voltage OVP, UVLO, and OCP
  - Open and Shorted LED Fault Detection
  - Thermal Shutdown
- Minimum Number of External Components



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# 2 Applications

- Industrial Backlighting Systems in Control Panels
- Test and Measurement Equipment

#### 3 Description

The TPS61193 is a high-efficiency, low-EMI, easy-touse LED driver with flexibility to support a wide range of applications. It has three high-precision current sinks that can be combined for higher current capability.

The TPS61193 has an integrated DC-DC supporting both boost and SEPIC mode operation. The converter has adaptive output voltage control based on the LED current sink headroom voltages. This feature minimizes the power consumption by adjusting the voltage to lowest sufficient level in all conditions. For EMI control the DC-DC converter supports spread spectrum for switching frequency and an external synchronization with dedicated pin.

The TPS61193 has wide input voltage range from 4.5 V to 40 V for robust support of different types of applications. The TPS61193 integrates extensive fault detection features. The device supports PWM brightness dimming ratio of 10 000:1 for 100-Hz input PWM frequency.

To request a full data sheet or other design resources: request TPS61193.

#### Device Information<sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
TPS61193	HTSSOP (20)	6.50 mm × 4.40 mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.



#### System Efficiency



## Simplified Schematic

TEXAS INSTRUMENTS

www.ti.com

### 4 Device and Documentation Support

#### 4.1 Device Support

#### 4.1.1 Development Support

Power Stage Designer<sup>™</sup> Tool can be used for both boost and SEPIC: http://www.ti.com/tool/powerstage-designer

#### 4.2 Documentation Support

#### 4.2.1 Related Documentation

For related documentation see the following:

- PowerPAD<sup>™</sup> Thermally Enhanced Package
- Understanding Boost Power Stages in Switch Mode Power Supplies
- Designing DC-DC Converters Based on SEPIC Topology

#### 4.3 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on *Alert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

#### 4.4 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E<sup>™</sup> Online Community *TI's Engineer-to-Engineer (E2E) Community.* Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

#### 4.5 Trademarks

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#### 4.6 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

#### 4.7 Glossary

#### SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

#### 5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PWP (R-PDSO-G20)

PowerPAD<sup>™</sup> PLASTIC SMALL OUTLINE



All linear dimensions are in millimeters. NOTES: Α.

- Β. This drawing is subject to change without notice.
- Body dimensions do not include mold flash or protrusions. Mold flash and protrusion shall not exceed 0.15 per side. C.
- This package is designed to be soldered to a thermal pad on the board. Refer to Technical Brief, PowerPad D.
- Thermally Enhanced Package, Texas Instruments Literature No. SLMA002 for information regarding recommended board layout. This document is available at www.ti.com <a href="http://www.ti.com">http://www.ti.com</a>. E. See the additional figure in the Product Data Sheet for details regarding the exposed thermal pad features and dimensions. E. Falls within JEDEC MO-153

PowerPAD is a trademark of Texas Instruments.



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