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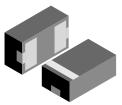
(5-2008)



Vishay Semiconductors

Bidirectional Asymmetrical (BiAs) Single Line ESD-Protection Diode in LLP1006-2L





20855

MARKING (example only)



Bar = pin 1 marking Y = type code (see table below) X = date code

FEATURES

- Ultra compact LLP1006-2L
- Low package height < 0.4 mm
- 1-line ESD-protection
- Working range -7 V up to +14 V or -14 V up to +7 V
- Low leakage current < 0.1 μA
- Low load capacitance typical C_D = 8 pF
- ESD-protection acc. IEC 61000-4-2
 ± 25 kV contact discharge
 ± 30 kV air discharge
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- e3 Sn
 Tin plated exposed side wall of leadframe.

 Soldering can be checked by standard vision inspection.
 (AOI = Automated Outgoing Inspection)
 No X-ray necessary
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION						
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY			
VCUT0714A-HD1	VCUT0714A-HD1-GS08	8000	8000			
VCUT0714AHD1	VCUT0714AHD1-G3-08	10 000	100 000			

PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	PIN PLATING	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VCUT0714A-HD1	LLP1006-2L	e4	В	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals	
VCUT0714AHD1	LLP1006-2L	e3	7	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals	

ABSOLUTE MAXIMUM RATINGS VCUT0714A-HD1							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	Pin 1 to pin 2 acc. IEC 61000-4-5, 8/20 µs/single shot	1	5	А			
	Pin 2 to pin 1 acc. IEC 61000-4-5, 8/20 µs/single shot	І _{РРМ}	2	Α			
Peak pulse power	Pin 1 to pin 2 acc. IEC 61000-4-5, 8/20 µs/single shot	D	63	W			
	Pin 2 to pin 1 acc. IEC 61000-4-5, 8/20 µs/single shot	P _{PP}	54	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 25	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD	± 30	kV			
Operating temperature	Junction temperature	T _J -40 to +12		°C			
Storage temperature		T _{STG}	-55 to +150	°C			

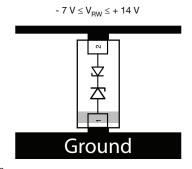
PATENT(S): www.vishay.com/patents

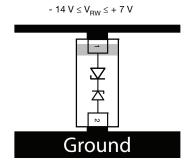
This Vishay product is protected by one or more United States and International patents.



CUT THE SPIKES WITH VCUT0714A-HD1

The VCUT0714A-HD1 is a bidirectional but asymmetrical (BiAs) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0714A-HD1 offers a high isolation (low leakage current, small capacitance) within the specified working range of -7 V to +14 V or -14 V and +7 V. Due to the short leads and small package size of the tiny LLP1006-2L package the line inductance is very low, so that fast transients like an ESD-strike can be clamped with minimal over- or undershoots.





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ELECTRICAL CHARACTERISTICS VCUT0714A-HD1 (pin 2 to pin 1) (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	_	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	14	V	
Reverse voltage	at I _R = 0.1 μA	V_R	14	-	-	V	
Reverse current	at V _{RWM} = 14 V	I _R	-	-	0.1	μΑ	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	14.5	-	-	V	
Reverse clamping voltage	at I _{PP} = 1 A	V _C	-	-	27	V	
	at I _{PP} = I _{PPM} = 2 A	V _C	-	-	30	V	
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	-	8	8.5	pF	
	at V _R = 7 V; f = 1 MHz	C _D	-	4	-	pF	

ELECTRICAL CHARACTERISTICS (pin 1 to pin 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	7	V		
Reverse voltage	at I _R = 0.1 μA	V_R	7	-	-	V		
Reverse current	at V _{RWM} = 7 V	I _R	-	-	0.1	μΑ		
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	7.3	-	-	V		
Reverse clamping voltage	at I _{PP} = 1 A	V _C	-	-	13	V		
	at I _{PP} = I _{PPM} = 5 A	V _C	-	-	17	V		
Capacitance	at V = 0 V; f = 1 MHz	C _D	-	8	8.5	pF		
	at V = 3.5 V; f = 1 MHz	C _D	-	6.4	-	pF		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

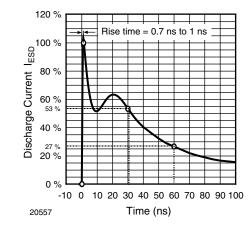


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω /150 pF)

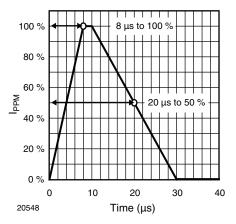


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

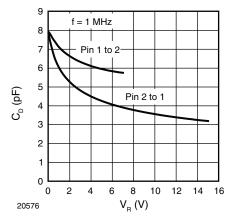


Fig. 3 - Typical Capacitance C_D vs. Reverse Voltage V_R

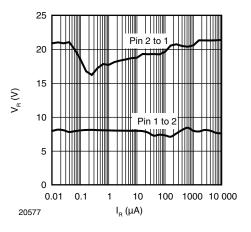


Fig. 4 - Typical Reverse Voltage V_{R} vs. Reverse Current I_{R}

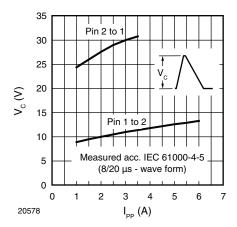


Fig. 5 - Typical Peak Clamping Voltage $V_{\rm C}$ vs. Peak Pulse Current $I_{\rm PP}$

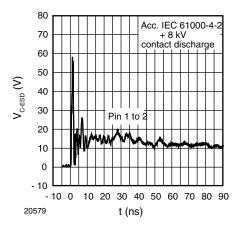


Fig. 6 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

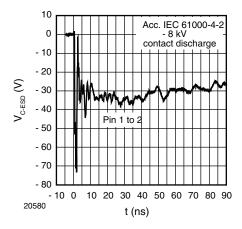


Fig. 7 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

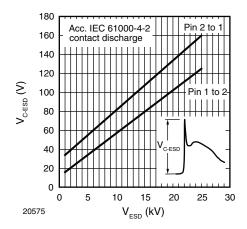
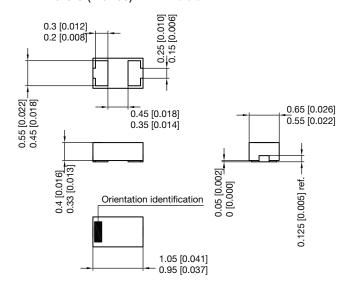
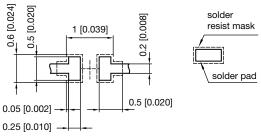


Fig. 8 - Typical Peak Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L



Foot print recommendation:



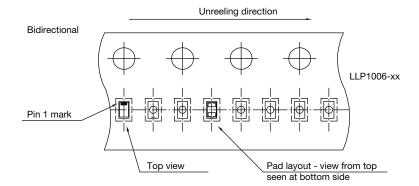
Pad Design Patented: (PUS 9.018.537 B2)

Document no.: S8-V-3906.04-005 (4)

Rev. 7 - Date: 11.May 2016 20812









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